
Chapter 1

Tables

Table 1.1 LEVELS OF ABSTRACTION COMPARISON

Digital Design Engineer's Level	Examples of Modeled Entity	Approximate Equivalence to Computer Architect's Level
system level	pipelines, instruction decoders, and TLBs	microarchitecture
register transfer level (RTL)	registers, buses, multiplexers, and combinational logic	microarchitecture and logical implementation
gate level or logic level	library cells of AND, OR, etc. gates	logical implementation
transistor level	transistors in various process technologies	chip
layout level	geometries, transistor placements	chip