

At A Glance

AMD Loses 287 Microcode Case	1
A San Jose jury has ruled that AMD does not have the right to use Intel's microcode in its 287 coprocessor. This ruling threatens to delay AMD's 486 and could make the legality of AMD's 386 dependent on the state and federal courts upholding the earlier arbitration award.	
Pocket Intelligence—The Next Killer Application	3
Two little-known RISC architectures are poised to exploit a window of opportunity created by an exciting new class of microprocessor applications.	
Most Significant Bits	4
SPARC International announces V9 spec; LSI adds FP to embedded MIPS processor; MIPS/SGI merger completed; P5 not to be called the 586?; Corollary shifts MP chip set to P5; Cypress announces SPARCset chip set; Toshiba demonstrates first RDRAM.	
Local Buses Poised to Enter PC Mainstream	7
VESA and Intel have both unveiled local-bus standards that promise to reshape the PC system architecture, but there will be competition between the two standards.	
DECWRL Celebrates 10 Years of Influence	14
Digital Equipment Corp.'s Western Research Lab has made many contributions to computer system design, and it is now working on a 1-GHz Alpha microprocessor.	
Futurebus+ Offers Innovative Protocols	16
The final part of our Futurebus+ series describes the development of its key contributions and the silicon support for the bus.	
Gonzo Marketing	20
Designing micros is easy. Finding clever ways to sell them is hard.	
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