

Resources

ISSCC 93 in San Francisco

The fortieth annual *International Solid-State Circuits Conference*, sponsored by the IEEE, will be held on February 23–26 at the San Francisco Marriott. On Tuesday, a full-day course, "Introduction to Computer Architecture," will be presented by Norm Jouppi, Steve Przybylski, and Mark Horowitz. The general sessions, held Wednesday through Friday, will cover the latest research in chips for communications, video, DRAMs, and processors. Unlike previous years, the microprocessor session does not appear to include previews of any major commercial products.

Early registration (before 2/8) for the conference is \$270, with IEEE members getting a \$20 discount. The Tuesday tutorial costs an additional \$250. Contact ISSCC at 701 Welch Road, Suite 2205, Palo Alto, CA 94304; 415/327-6622, fax 415/321-9692.

New Programs from University Video

University Video Communications (UVC) provides a series of computer-science videotapes suitable for graduate courses or corporate education. The latest releases include Sun's Dave Ditzel on 64-bit SPARC, HP's Josh Fisher on instruction-level parallelism, Gordon Bell and Carver Mead on creativity and innovation, and Mike Blasgen and Rich Oehler of IBM on the evolution of PowerPC. Previously-released tapes discuss both architecture and implementations of Alpha, MIPS, PA-RISC, SPARC, and POWER.

These 50-minute tapes are priced at \$50 each, with discounts for quantity purchases. For a complete catalog, contact UVC at PO Box 5129, Stanford, CA 94309; 415/813-0506, fax 415/813-0506, or send Internet mail to uvc.lemon@forsythe.stanford.edu.

Buscon Joins Softcon in San Jose

This year, Buscon '93 West features a concurrent show for software developers called Softcon. Both events, sponsored by *Electronic Design*, cover various bus architectures, including chip sets, drivers, backplanes, development tools, boards, and systems. Softcon sessions also look at real-time operating systems and embedded applications. A third event, ChannelExpo, focuses solely on the Micro Channel architecture. All three events will be at the San Jose Convention Center on February 9–11. Workshops will be held on February 8.

Early registration (before 1/15) for the three-day conference is \$445, or \$595 including the full-day pre-conference workshops. Single-day and half-day rates are also available. Admission to ChannelExpo sessions only costs \$150, with a discount for MCDA members. For more information, contact CMC Registration, 200 Connecticut Avenue, Norwalk, CT 06856; 800/243-3238 or 203/852-0500, fax 203-857-4075.

Micro Channel Documents Available

Those interested in ChannelExpo may also want to take advantage of several new documents available from the Micro Channel Developers Association (MCDA), including revision 2.0 of the Architecture Specification, a Mechanical Specification, an Electrical Specification, and others. Also available are proceedings from a recent Micro Channel seminar and from the working group meetings.

The prices of the specifications range from \$25–\$50 for MCDA members, and \$45–\$85 for non-members. The cost of the proceedings is \$300–\$600 for members and \$580–\$990 for non-members. Contact MCDA at 2280 North Bechelli Lane, Suite B, Redding, CA 96002; 800/GET-MCDA or 916/222-2262, fax 916/222-2528.