

At A Glance

Cyrix Prepares 486DX-Compatible CPU	1
Cyrix's new "M7" processor matches Intel's 486DX line feature-for-feature. The company is pitching its new chips for designs with 50-MHz system buses, an area that Intel has de-emphasized.	
Editorial: Digital TV No Panacea for CPU Vendors	3
Many processor vendors are eagerly pursuing design wins for interactive digital TV devices, but this market will take years to develop and might never produce PC-type volumes and profits.	
Most Significant Bits	4
IBM pushes PowerPC into embedded market; IBM to develop RISC-based PCs and AS/400s; Intel attempts to patent upgrade socket; AMD rumored to have 50-MHz 486SX2; Intel, ATI team on multimedia standard; Motorola DSP targets digital cellular phones; Tom Whiteside takes over at MIPS; BAPCo releases new benchmarks; Errata: Destiny address.	
TFP Designed for Tremendous Floating Point	9
MIPS' new TFP chip set contains many unique design features to boost floating-point performance and cache bandwidth. It is also the most aggressively superscalar processor yet announced.	
SH7000 Architecture Bends RISC Rules	14
Hitachi's SH7000 architecture is designed specifically for PDA applications. CPU size and power are kept small by using 16-bit fixed-length instructions and a simple architecture.	
Hitachi Previews First PA-RISC Processor	19
HARP-1 is Hitachi's first PA-RISC chip. It uses a significantly different design from HP's PA-RISC processors but ultimately achieves similar performance with a larger die.	
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Publisher and Editor-in-Chief

Michael Slater

e-mail: mslater@mdr.ziff.com

Senior Editor

Linley Gwennap

408/720-8840 • Fax 408/720-9465

e-mail: linley@mdr.ziff.com

Associate Editors

Brian Case Mark Thorson

Customer Service Manager

Richard Lang

Customer Service

Colleen McMillen Barbara Roberts

Office Manager: Loralee Denny

Production Manager: Ron Liskey

Production Artist: Rachel Dufour

Contributing Editors

Dennis Allison	Rich Belgard
Jeff Deutsch	Morris Enfield
Mike Feibus	Bruce Koball
Dean McCarron	George Morrow
Bernard L. Peuto	John Snell
Nick Tredennick	John F. Wakerly
John H. Wharton	

European Operations

Phil Sutcliffe, Parkway Gordon

Phone 44 (0)491 875386

Fax 44 (0)491 875524

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MICRODESIGN

874 Gravenstein Hwy. So., Suite 14
Sebastopol, California 95472

Phone 707/824-4004 • Fax 707/823-0504

Customer service: 800/527-0288

e-mail: cs@mdr.ziff.com



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