## Recent IC Announcements

| Part Number | Vendor | Description | Price/Quantity | Availability |
| :---: | :---: | :---: | :---: | :---: |
| General-Purpose Microprocessors |  |  |  |  |
| Cx486DRx2 | $\begin{aligned} & \text { Cyrix } \\ & 800.527 .4614 \end{aligned}$ | 486-like clock-doubled processor with 386DX-style footprint. Marketed by Xerox for Cyrix (number shown is for Xerox). | $\begin{aligned} & \$ 399 / 1 \\ & (25 / 50 \mathrm{MHz}) \end{aligned}$ | Prod.-Now |
| Embedded Microprocessors |  |  |  |  |
| 4PD7800x <br> $\mu$ PD7801x | $\begin{aligned} & \text { NEC } \\ & 800.366 .9782 \end{aligned}$ | Low-end 8-bit microcontrollers with up to 16 K ROM ('00) or 32 K ROM ('01). Operates from 2.7 to 6 V . '01 has 8-bit ADC. | $\begin{aligned} & \$ 7.75 / 10 \mathrm{~K} \\ & (' 01) \end{aligned}$ | Prod.-Now |
| Z86L72 | $\begin{aligned} & \text { Zilog } \\ & \text { 408.370.8000 } \end{aligned}$ | Member of Z8 family of 8 -bit microcontrollers intended for remote controls. 16 K ROM, 768 bytes RAM. Operates down to 2 V . | \$4/100K | Prod.-10/93 |
| Memory |  |  |  |  |
| $\begin{aligned} & \text { KM48xxx00 } \\ & \text { KM416xxx00 } \end{aligned}$ | $\begin{aligned} & \text { Samsung } \\ & \text { 408.954.7000 } \end{aligned}$ | $2 \mathrm{M} \times 8$ (KM48') and $1 \mathrm{M} \times 16$ (KM416') DRAMs for operation at 3 or 5 V . Speeds are 60 ( 48 ' only), 70,80 , and 100 ( $416^{\prime}$ only) ns. | $\begin{aligned} & \$ 155 / 100 \\ & (416,70 \mathrm{~ns}) \end{aligned}$ | Samples-Now <br> Prod.-3Q93 |
| STK12C68 | $\begin{aligned} & \text { Simtek } \\ & 719.531 .9444 \end{aligned}$ | $8 \mathrm{~K} \times 8$ SRAM with on-chip EEPROM backup. On power loss, data is copied to EEPROM . Speeds are 30,35 , and 45 ns . | \$10.49/1K | Prod.-Now (45 ns) |
| 24AAxx | Microchip $602.786 .7200$ | 1.8 V serial EEPROMs in 1 - ('01), 2- ('02), 4- ('04), 8- ('08) and 16-bit ('16) sizes. $3 \mu \mathrm{~A}$ typical standby current. | \$2.14/1K | Prod.-Now <br> ('16) |
| M5M28F400 | $\begin{aligned} & \text { Mitsubishi } \\ & 408.730 .5900 \end{aligned}$ | $256 \mathrm{~K} \times 16$ flash memory for $5-\mathrm{V}$ operation. 30 mA maximum power. 12 V req'd for erase and prog. Speeds are 100, 120, and 150 ns . | \$40/10K <br> (100 ns) | Samples-Now <br> Prod.-4Q93 |
| $\begin{aligned} & \text { M66251 } \\ & \text { M66253 } \end{aligned}$ | $\begin{aligned} & \text { Mitsubishi } \\ & 408.730 .5900 \end{aligned}$ | $5 \mathrm{~K} \times 8$ FIFOs. Speed is 30 ns access, 40 ns cycle. | $\begin{aligned} & \$ 10 / 1 \mathrm{~K} \\ & (' 251) \end{aligned}$ | Prod.-Now |

## Peripheral Chips

| 82C611 | Opti |
| :--- | :--- |
|  | 408.980 .8178 |
| 92C108 | Opti |
|  | 408.980 .8178 |
| Supernet | AMD |
| 2 chip set | 408.749 .5703 |
| ML4644 | Micro Linear |
|  | 408.433 .5200 |
| SA7189 | Philips |
| SA7196 | 408.991 .4577 |

32-bit VL-Bus compatible SCSI interface chip. Transfer rate
up to 10 Mbyte/s.
32-bit VL-Bus compatible SCSI interface chip with bus master
capability. Transfer rate up to 40 Mbyte/s.
Four-chip set for twisted-pair FDDI interface. Speed is 100 Mbps.
Upward-compatible and same price as Supernet 1 .
Ethernet multiplexer capable of handling four attachment unit
interfaces (AUIs) sharing one Ethernet transceiver.
Digital YUV-to-NTSC, PAL, or S-video converter ('7189) and digital
video decoder, scaler, and clock generator ( 7196 ).

## Interface

| MT8841 | Mitel |
| :--- | :--- |
|  | 613.592 .2122 |
| ADXL50 | Analog Devices |
|  | 617.937 .1428 |
| TMP-01 | Analog Devices |
|  | 617.937 .1428 |
| ADM69x | Analog Devices |
|  | 617.937 .1428 |
| AD1890 | Analog Devices |
| AD1891 | 617.937.1428 |
| AD7008 | Analog Devices |
|  | 617.937 .1428 |

Caller ID decoder chip with serial interface.
Micromachined silicon accelerometer with integral signal
conditioning circuitry. 5-V operation, analog output.
Thermostat chip with integral temperature sensor and open-
collector outputs. Programmed with external resistors.
Microprocessor supervisory circuits for power management.
Second-source of popular devices from Maxim.
20- ('90) and 16-bit ('91) serial A/D converters for high-end audio
use. Includes on-chip DSP filtering and buffering FIFO.
Integrated 10-bit DAC and digital synthesis circuit for RF telecom,
modems, and spread-spectrum.

| \$6/1K | Samples-Now |
| :---: | :---: |
| \$62/1K | Prod.-9/93 |
| \$20/1K | Samples-Now |
| \$12.54/1K | Prod.-Now |
| $\begin{aligned} & \text { \$29/1K ('89) } \\ & \$ 32 / 1 \mathrm{C} \\ & \text { ('96) } \end{aligned}$ | Prod.-Now |
| \$2.78/1K | Samples-Now |
| \$39/100 | Prod.-Now |
| \$2.75/100 | Prod.-Now |
| \$1.95/5K | Prod.-Now |
| $\begin{aligned} & \$ 34 / 1 \mathrm{~K} \\ & (' 1890) \end{aligned}$ | Prod.-Now |
| \$20.25/1K | Prod.-Now |

