Recent IC Announcements

Part Number	Vendor	Description	Price/Quantity	Availability
Interface				
AD871	Analog Devices 617.937.1428	Complete 12-bit acquisition system on a chip operates at 5 MSPS with RMS noise of 0.16 LSB. Pin-compatible with AD872.	\$95/1,000	Prod.—Now
DAC16	Analog Devices 617.937.1428	High-speed 16-bit D/A converter produces analog waveforms at 2 MHz, has settling time of 500 ns, maximum noise equivalent to 0.3 LSB.	\$21.75/1,000	Prod.—Now
<u>Memory</u>				
M5M28F016	Mitsubishi 408.730.5900	Low-voltage 16M flash RAM is organized as 2Mx8, divided into 32 blocks of 64K. Device runs on 3.3 V, requires 12 V for programming.	\$90/10,000	Samples—Now Prod.—3Q94
3Q94W73B586	IC Works 408.922.0202	Synchronous cache data RAMs have 9-ns access time, support 486 and Pentium burst sequences at 66 MHz, are organized as 32Kx18.	\$50/1,000	Prod.—Now
Programmabl	le Logic			
MACH210A-7	AMD 800.222.9323	New version of MACH PLD has pin-to-pin delay of 7.5 ns, supports counter frequency of 133 MHz, has 64 macrocells, 6 inputs, 32 I/Os.	\$10.75/1,000	Prod.—Now
AT6002 AT6005	Atmel 408.441.0311	FPGAs for building adaptive hardware provide for dynamic in-system reconfiguration. Chips have 2,000 and 5,000 gates, 2- and 4-ns speeds.	\$16/5,000 \$72/5,000	Prod.—Now
System Logic	<u>:</u>			
ET5001 ET5002	ETEQ 408.432.8147	TIGER chip set provides system logic for interfacing Pentium to VL-bus. Incorporates 64-bit 2nd-level cache controller with write-back policy.	\$30/10,000	Prod.—Now
W42C08	IC Works 408.922.0202	Dual-output clock generator for x86, Pentium, and RISC processors. Generates a reference frequency and a synthesized 2–120-MHz output.	\$1.62/10,000	Prod.—Now

Recent IC Announcements Vol. 8, No. 5, April 18, 1994