

Recent IC Announcements

Part Number	Vendor	Description	Price/Quantity	Availability
DSPs				
1616	AT&T 800.553.2448	DSP includes flash memory on chip in place of ROM, is pin- and code-compatible with ROM-based version, achieves 50 MIPS at 5 V.	\$1,000/10	Samples—Now Prod.—4Q94
IS-54	NEC 415.965.6367	Chip set based on μ PD7701x 16-bit DSP implements time-division multiple-access for digital cellular communications, available in 3-V version.	\$60/100,000	Samples—4Q94 Prod.—1Q95
1200A	Star 408.526.2160	Enhanced version of SPROC processor has single-cycle multiply, achieves 18 MIPS at 33 MHz, has 8 ports, 512 bytes RAM. Also 50-MHz version.	\$10.77/10,000	Prod.—Now
89321	Zilog 408.370.8246	Fixed-point DSP is 16 bits wide, achieves 20 MIPS at 20 MHz, allows two data accesses and one instruction access per cycle, has wait generator.	\$2.95/100,000	Prod.—Now
Embedded Microprocessors				
DS87C520	Dallas 214.450.0448	Processor is software-compatible with 8051 but executes instructions in four clocks, achieves 8 MIPS at 33 MHz, includes 16K EPROM, 1.2K SRAM.	\$14.60/1,000	Samples—Now Prod.—3Q94
8XC576	Philips 408.991.5207	Derivative of 80C51 incorporates 10-bit A/D converter, UPI interface, 8K of EPROM, 256 bytes of RAM, two PWM outputs, Schmitt trigger inputs.	\$4.90/5,000	Prod.—Now
Memories				
CY27H010	Cypress 408.943.2600	Family of EPROMs organized as 128K \times 8 has access times as fast as 25 ns, typically uses 660 mW, requires 12.75-V programming voltage.	\$30.10/100	Samples—Now Prod.—3Q94
SP95P0xC	SGS-Thomson 617.259.0300	Family of serial EEPROMs organized as 1K \times 8 is compatible with SPI bus, has 16-byte page size, endures 1 million erase/write cycles.	\$1.45/1,000	Prod.—Now
M5M51016ALL	Mitsubishi 408.730.5900	SRAM organized as 16K \times 16 has typical standby current of 1 μ A at 3 V, available with access times of 70, 85, and 100 ns.	\$15.75/10,000	Prod.—Now
M5M408LL	Mitsubishi 408.730.5900	SRAM organized as 512K \times 8 has typical standby current of 0.4 μ A at 3 V, available in 55-, 70-, 85-, 100-, and 120-ns speed grades.	\$96.00/10,000	Prod.—Now
Miscellaneous				
LT1312	Linear	Single- and dual-voltage regulators for PCMCIA card slots provide 0-, 3.3-, 5-, 12-V, and high-impedance outputs from a 13–20-V supply.	\$2.02/10,000	Prod.—Now
LT1313	408.432.1900			
SN74CBT3244	TI 800.477.8925, x4500	Byte-wide CMOS switch device has "on resistance" of 5 Ω , propagation delay of 250 ps. Also 10-bit-wide version.	\$1.30/1,000	Prod.—Now
ALDC1	IBM 914.892.5389	Data-compression chip uses ALDC to achieve average 2-to-1 compression at 20 Mbytes/s, selectable 8- or 16-bit bus. Also 40-Mbyte/s version.	\$38.30/10,000	Samples—Now Prod.—3Q94
System Logic				
M74HCT24xPU	SGS-Thomson 617.259.0300	Series of CMOS logic devices incorporates constant-current sources to eliminate external pull-up resistors, has quiescent current of 200 μ A.	\$0.47/1,000	Prod.—Now
SN74GTL166xx	TI 800.477.8925, x4500	Family of logic devices provides 18-bit-wide interface between TTL and GTL buses, incorporates edge-rate control to minimize EMI.	\$6.90/1,000	Prod.—Now
SN74ALVC16245	TI 800.477.8925, x4500	CMOS bus transceiver is 16 bits wide, has typical propagation delay of 2.2 ns, operates from 3.3-V supply.	\$2.55/1,000	Prod.—Now
VT82C496G	VIA	Chip set provides system logic; interfaces 486 CPU to cache, fast-page-mode DRAM, ISA bus; has VL-bus IDE controller.	\$18/10,000	Prod.—Now
VT82C406MV	510.683.3300			
TQ20xx	TriQuint 408.982.0900, x142	Clock generator intended for Alpha processors is available in speed grades to 700 MHz, has typical period-to-period output jitter of 25 ps.	\$11.75/10,000	Samples—Now Prod.—3Q94
TQ10xx	TriQuint 408.982.0900, x142	Family of clock buffers intended for PowerPC processors operates at 180 MHz, has 11 outputs with a variety of multiplied and divided clock rates.	\$16/10,000	Samples—Now Prod.—3Q94