Resources

MicroDesign Expands Technical Library

Two new industry reports will soon be released by Micro-Design Resources, publisher of *Microprocessor Report.* Both offer an in-depth analysis of the technical as well as the commercial aspects of the microprocessor industry, from a microscopic view to a telescopic overview.

The Complete x86, subtitled *The Definitive Guide to 386, 486, and Pentium-class Microprocessors,* covers both the history and the projected future of x86 processors. Its 700 pages and 300 illustrations analyze technical issues including architectural overviews, implementation details, and thorough performance comparisons. All major x86 chip producers (Intel, AMD, TI, Cyrix, and IBM) are profiled, with their technological and commercial strengths and weaknesses highlighted.

Included are more than 300 pages of technical analysis of 50 different 386 and 486 processors, plus another 100 pages devoted to Pentium-class chips. A special section reviews patent law and litigation regarding x86 processors.

Also available in December is **RISC on the Desktop,** subtitled *A Comprehensive Analysis of RISC Microprocessors for PCs, Workstations, and Servers.* The 500-page report scrutinizes the major RISC chip makers, operating systems, and system vendors. The technical sections cover architecture and implementation details of the full range of PowerPC, SPARC, MIPS, PA-RISC, and Alpha processors, as well as Pentium, the leading CISC alternative.

Both reports include a full description of the MPR Cost Model and the data used to estimate chip costs for each device. The reports are priced at $2,195 each before December 15 or $2,695 thereafter, with discounts for MPR subscribers. Contact MDR (Sebastopol, Calif.) at 707.824.4001 or 800.527.0288; fax 707.823.0504 or e-mail *info@mdr.ziff.com*.

Still Time for Parallel Parking in San Jose

The subject of the 27th annual **IEEE/ACM International Symposium on Microarchitecture** will be instruction-level parallelism. The three-day symposium will be held at the Fairmont Hotel in San Jose from November 30 to December 2. The keynote address will be delivered by Wen-Mei Hwu, who also presented at the recent Microprocessor Forum. Special hotel rates are available to attendees. Registration fees are $345 for IEEE/ACM members, $430 for nonmembers, and $240 for students. Register via fax at 408.765.6415, or send e-mail to *farrens@cs.ucdavis.edu*. For more information, send e-mail to Jan Harper, *micro27@austin.intel.com*.

Microcomputer News over the Wire

Situation Publishing produces **The Register**, an electronically distributed newsletter covering developments in the semiconductor, microcomputer, and microprocessor industries. Delivery is every two weeks.

A subscription is free of charge "for the foreseeable future." To subscribe, simply send an empty message to *connect@magee.demon.co.uk*. Physical mail can be sent to 18 Grafton Road, Harrow, Middlesex HA1 4QT, U.K.

Editor, Bored, Boards Board Magazine

Editor Warren Andrews, late of Computer Design, has thrown in his lot with Concept Development as editor-in-chief of **The Real Times** magazine. The inaugural issue of the bi-weekly magazine is due in January (1995). Coverage will be devoted to "real-time, open systems solutions," including VME, ISA/EISA, Multibus, PCI, and other bus architectures. He will continue to contribute to *Embedded Computer Trends,* a monthly newsletter that has subsumed *InfoBUS Report.*

Subscriptions cost $295. For more information, call William O'Brien at 617.449.2033; fax 617.449.1193.