

Resources

What the HEC, Go See Windows Hardware

The 1995 Windows Hardware Engineering Conference (**WinHEC**) comes to San Francisco in March, promising three days of technical sessions on designing systems for Microsoft Windows and Windows NT. The exhibition features products from a wide range of PC technology suppliers.

Featured attractions include presentations by Microsoft and Intel personnel, a keynote address by Bill Gates, and our own Michael Slater on "Hardware Technology and System Design Trends." The third day offers four seminars including "Selecting CPUs for Windows-Based PCs and Servers," also with Michael Slater.

The conference will be held at the Moscone Center in San Francisco, March 20–22. Registration costs \$1,045 for all three days, \$795 for the two-day conference, or \$500 for just the seminars. To register, or for more information, call WinHEC (San Francisco, Calif.) at 800.421.6338 or 415.979.2267; fax 415.979.2270.

Computer Conference Tops San Francisco

The theme of this year's IEEE computer system conference is "Technologies for the Superhighway." The three-day gathering of industry experts, scientists, and visionaries will be held at the Stanford Court Hotel, high atop San Francisco's Nob Hill, March 6–8. **Compcon '95** features presentations on Digital's 21164, PowerPC multiprocessing, and the PA-8000. General Magic's Steve Schramm will cover "Agents That Travel," and Apple will discuss its InfoPad project. Two additional days (3/5, 3/9) are devoted to tutorial sessions.

Three-day registration costs \$325 for IEEE members or \$425 for nonmembers, before 2/21. The student rate is just \$50. For more information, call LLNL (Livermore, Calif.) at 510.422.2199; fax 510.422.2495 or send e-mail to compcon95@lbl.gov.

Super Designers Convene in Santa Clara

The Santa Clara Marriott is the chosen site for **Design SuperCon '95**, a three-day, three-way conference for systems engineers, chip designers, and communications aficionados. Seminars will feature presentations by *Microprocessor Report's* Jim Turley and MPR contributors Nick Tredennick and Phil Lapsley.

More than 150 seminar programs are scheduled in three parallel tracks, focusing on high-performance systems and buses, data communications and networking, and chip-level ASIC and FPGA design. Wednesday's session on "Microprocessors for Internetworking" concludes with an expert panel hosted by Jim Turley.

The conference will be held February 28–March 2. Registration costs \$235 for all three days, \$175 for two days, and \$115 for a single day. For more information, call Design SuperCon (Boulder, Colo.) at 303.530.4562, or 800.772.7842; fax 303.530.4334.

Jump on the Bus to the PCI Conference

Nestled in the heart of Silicon Valley, The Westin Hotel in Santa Clara will host **PCI Week** March 27–31. Attendees can select from a cluster of half-day sessions on the Monday opening the show. Topics will include "PCI Card Packaging" and "How to Make PCI Work on Host Systems." Tuesday and Wednesday feature the PCI Special Interest Group's **PCI Developers' Conference**, focusing on technical matters for design engineers, including PCI compliance and testing. Thursday and Friday will be devoted to **PCI '95**, two days focused on the technology and methods required to design, manufacture, and market PCI-based components and systems.

Admission to all five days (before 2/28) costs \$895, or \$495 for a two-day package. For more information, contact Annabooks, Inc. (San Diego, Calif.) at 800.462.1042; send e-mail to 73204.3405@compuserve.com.