

Resources

Computers in Il Futuro

The 22nd **International Symposium of Computer Architecture** will be held this year in Santa Margherita Ligure, Italy. The annual event, sponsored by the ACM and the IEEE Computer Society, runs a full week from June 18–24 and will cover high-performance microprocessors, multiprocessors, cache coherence, instruction-level parallelism, and more.

Early registration (before 5/15) costs \$370 for ACM/IEEE members, \$465 for nonmembers. For more information, call 212.626.0616; fax 908.582.4417 or access <http://www.cs.wisc.edu/~arch/www/isca95>.

Bug Hunters on the Net

You can download **UCBTEST**, a suite of programs for “testing certain difficult cases of IEEE-754 floating-point arithmetic,” free of charge. The programs were developed using number-theory algorithms first developed by Turing Award–winner Prof. W. Kahn at the University of California, Berkeley.

The suite is available via ftp from [netlib.att.com](ftp://netlib.att.com) in [/netlib/ftp/ucbtest.tar.gz](ftp://netlib/ftp/ucbtest.tar.gz), or from the World Wide Web at <http://netlib/att.com/netlib/ftp/index.html>. The file is about 1M and requires gunzip to decompress.

New Chips Tape Out

University Video Communications has released its Distinguished Lecture Series: **Leaders in Computer Science and Electrical Engineering**, a catalog of video tapes covering design automation, databases, and virtual reality. Also new from UVC is a selection of video papers from the August 1994 Hot Chips symposium, including presentations on the i960, 68060, Pentium, PowerPC 604, Alpha 21164, and Thunder. All tapes cost \$30. To order, contact Judith Lemon at 415.813.0506 or send e-mail to lemon@hera.eecs.berkeley.edu.

See DSP Chips Live...

The **DSPX '95** conference features more than 150 presentations from industry leaders, visionaries, and pioneers in the digital-signal-processing industry. Held May 16–18 in the San Jose (Calif.) Convention Center, the show promises three days of seminars, exhibits, tutorials, and workshops for DSP neophytes and professionals.

Conference registration runs \$795 for all three days, or \$595 for a single day. For more information, contact Reed Exhibition Companies (Carrollton, Texas) at 203.840.5652; fax 214.245.8700.

...or Read about DSP Development Tools

Berkeley Design Technology (BDT) has released the 1995 edition of its industry report, **DSP Design Tools and Methodologies: The Buyer's Guide**. This three-volume, 1,300-page report provides analyses of more than 350 different DSP tools from 130 companies. It is intended for DSP designers, tool vendors, and manufacturers interested in digital signal processing.

Single copies of the report cost \$3,850; additional copies are discounted by \$500. A brochure is available by calling BDT (Fremont, Calif.) at 510.791.9100; fax 510.791.9127 or send e-mail to toolreport@bdti.com.

Custom Chip Show in Santa Clara

CICC '95, the Custom Integrated Circuits Conference, is coming to the Santa Clara (Calif.) Convention Center May 1–4. Educational sessions will cover high-performance digital design, analog, wireless, communications issues, and application-specific DSP.

Registration costs \$275 for IEEE members or \$330 for nonmembers and includes a copy of proceedings and admission to all sessions and exhibits. To register, or for more information, contact CICC (Washington, DC) at 202.986.2166; fax 202.986.1139.