RESOURCES

■ ISSCC Returns to San Francisco

The 1996 edition of the annual International Solid-State Circuits Conference (ISSCC '96) returns to the Marriott Hotel in downtown San Francisco February 8–10. This year's show is themed "System on a Chip" and features the usual assortment of SRAMs, PLLs, latches, and clock circuits in addition to the first disclosure of an upgraded PowerPC 603e microprocessor.

Registration fees run \$325 for IEEE members; \$350 for nonmembers. Admittance to the nine tutorials costs an additional \$50 per session. For more information, or to register, contact ISSCC (Palo Alto, Calif.) at 415.327.6622, or fax 415.321.9692.

■ Wireless Conference Keeps Things Moving

The Arizona Biltmore in Phoenix will host **Mobile Insights '96,** a conference devoted to mobile and wireless computing and communication. The two-day conference begins February 19 and features nearly two dozen presentations by industry analysts, editors, and business leaders. Featured speakers include Motorola VP Tom Beaver, Dan Rosen of Microsoft, and Compaq General Manager Michael Winkler.

Registration costs \$1,795 for both days, with discounts available for two or more people registering together. For more information, or to register, contact Mobile Insights (Cupertino, Calif.) at 800.419.0413 or 408.253.6600; fax 408.253.6608.

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■ Compcon Heads to Santa Clara

After 40 straight years in San Francisco, the IEEE **Compcon'96** show is moving a few miles south, to Santa Clara. The four-day conclave will be held at the Westin Hotel February 25–28, beginning with six day-long tutorials. This year's focus is on "Technologies for the Information Superhighway," which includes Web servers, mobile systems, workflow management, multimedia authoring and applications, and search engines.

Early registration (before 2/9) costs \$325 for IEEE members, \$425 for nonmembers. Late registration adds \$50. Tutorial sessions are also priced at \$325 for members, \$425 for nonmembers. For more information, or to register, contact registrar Dave Hunt (Livermore, Calif.) at 510.422.2199; fax 510.422.2495, send e-mail to compcon96@lbl.gov, or visit the Web at www.cse.ucsc.edu/~lisa/compcon96.html.

CalTech Provides Strategic Planning

The California Institute of Technology is offering a six-day executive program entitled "Integrated Strategic Planning." Covering marketing, R&D, and production strategies, the course is held over a span of seven months: 1/29–1/30, 4/30–5/1, and 7/24–7/25. Attendance is limited to 30 attendees.

Registration costs \$1,295 and covers all six days of instruction, parking, and meals. For more information, contact Caltech (Pasadena, Calif.) at 818.395.4043; fax 818.795.7174.



AUDIO TAPE

Is There Room for RISC After Pentium Pro?

Linley Gwennap, Editor in Chief Microprocessor Report

Even if you missed the MicroDesign Resources Dinner presentation by Linley, you can still hear all the details. For just \$49.00 (plus tax), MicroDesign Resources is offering an audio cassette of the presentation that includes the complete Q&A session with the attending audience.

Included in the \$49.00 price is a complete set of the handouts distributed at the event.

Linley's discussion includes how Intel gained the performance lead, whether it can sustain the lead, and what RISC vendors can do to withstand the onslaught of Pentium Pro.

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