RECENT IC ANNOUNCEMENTS

| PART NUMBER | VENDOR | DESCRIPTION | PRICE/QUANTITY | AVAILABILITY |
|------------------------|-----------------------------------|--|----------------|----------------------------|
| WARP11 | SGS-Thomson 617.259.0300 | Fuzzy-logic Weight-Associative Rule Processor (WARP) has 16 inputs and outputs, handles 256 rules, and operates at 40 MHz; in PLCC-68. | \$18.12/5,000 | Prod.—Now |
| INTERFACE | | | | |
| DB87144 | Databook 508.762.9779 | Single-chip host interface controller supports CardBus (PCI on PCMCIA) and proposed Zoomed video specification; complies with PCMCIA 2.1. | \$21/1,000 | Samples—Now Prod.—2Q96 |
| CL-CD1865 | Cirrus Logic 510.226.2140 | Eight-channel communications controller has 24 eight-byte FIFOs and improved serial data rates; compatible with CD180 and CD1864. | \$25/1,000 | Samples—Now Prod.—2Q96 |
| AD421 | Analog Devices 617.937.1428 | Loop-powered 16-bit DAC sends 4–20-mA signals to microcontrollers or control devices; with voltage regulator for 3-V, 3.3-V, or 5-V outputs. | \$6.95/1,000 | Prod.—Now |
| MEMORY | | | | |
| MPC27T416 MPC27T415 | Motorola 512.933.7726 | Fast synchronous SRAMs for L2 cache subsystems, with 9-, 10-, and 12-ns access times; organized as 16K×16 ('416) or 16K×15 ('415). | \$28.88/10,000 | Samples—2Q96 Prod.—3Q96 |
| IDT7281 IDT7282 | IDT 800.345.7015 | Dual asynchronous FIFOs are housed in small, 56-lead TSSOP packages; with 15-, 20-, and 25-ns access times; 512×9 ('281) or 1K×9 ('282). | \$6.40/5,000 | Prod.—Now |
| HN62W5016 | Hitachi 800.285.1601 | Burst-mode masked ROM has 16-Mbit capacity and can be configured for either ×16 or ×32 organization; 40-ns access time and 3.3-V supply. | \$9/10,000 | Prod.—Now |
| ISD2560 | ISD 408.369.2422 | ChipCorder memory IC is designed for voice recording and playback using 256-level voltage format per cell, eliminating external A/D or D/A. | \$13.55/1,000 | Prod.—Now |
| MISCELLANEO | US | | | |
| ML4878 | Micro Linear 408.433.5200 | Backlight chip for liquid-crystal displays reduces external component count and reduces leakage current to extend battery life; in SSOP-14. | \$3.95/1,000 | Prod.—Now |
| LTC1480 | Linear Technology 408.432.1900 | RS-485 transceiver operates from a single 3.3-V supply while providing 10kV ESD protection; part draws 300 μA supply current; in SO-8 package. | \$2.55/1,000 | Prod.—Now |
| X25045 | Xicor 408.432.8888 | Microprocessor supervisor for Intel microcontrollers provides reset gen- eration, 512 bytes of EEPROM, and watchdog timer; in SOIC-8 package. | \$1.50/10,000 | Prod.—Now |
| 78Q8392L | Silicon Systems 800.624.8999 | Transceiver chip for coaxial Ethernet is compliant with Ethernet II standards 802.3, 10BASE5, and 10BASE2; part draws 50 mA while transmitting. | \$2.60/1,000 | Prod.—Now |
| 78Q2250 | Silicon Systems 800.624.8999 | Line transceiver for ATM provides full transmit/receive facility with line- interface and clock-recovery functions at 155.52 Mbits/s over UTP wiring. | \$32/1,000 | Prod.—Now |
| VSC7125 | Vitesse 805.388.3700 | Integrated Fibre Channel transceiver operates from a single 3.3-V supply while drawing 650 mW at 1.0625 Gbps; in H-GaAs IV process. | \$37/1,000 | Prod.—Now |
| PROGRAMMABLE LOGIC | | | | |
| MACH111 MACH231 | AMD 800.222.9323 | Fast CPLDs have guaranteed 5-ns ('111) or 6-ns ('231) maximum pin-to-pin delays; with 32 or 64 macrocells and 44 or 84 pins. | \$17/1,000 | Prod.—Now |
| XC4000EX | Xilinx 408.559.7778 | Programmable logic family extends to 125,000 usable gates with selectable RAM cells; parts are PCI compliant and operate at 66 MHz. | \$695/100 | Samples—Now Prod.—4Q96 |