

■ **Embedded Systems Fall in San Jose**

The **Embedded Systems Conference West (ESC) '97** is coming to the San Jose (Calif.) Convention Center. Starting September 29, the four-day conference is billed as "a Disneyland for engineers." Following a day of tutorials and a keynote address by John C. Dvorak, ESC attendees can wander the exhibit halls and choose from well over 100 classes. Topics for the free-running 90-minute classes include translating from Java to C, hardware/software codesign, reducing run-time overhead in C++ programs, Java in embedded systems, Windows CE in embedded systems, Ethernet, fuzzy logic, and our own Jim Turley on selecting an embedded microprocessor.

Early registration (before September 5) runs \$895 for the three-day conference plus classes, \$445 for the all-day tutorials, or \$1,245 for both. An exhibits-only pass is free. To register or to get more information, contact ESC (Carrollton, Texas) at 972.620.3011 or visit www.embedsyscon.com.

■ **Handheld Industry Goes Up a Pole**

To find out how, attend the **Handheld & PDA Forum** August 20-22 at the St. Louis Marriott Pavilion Downtown. Covering mobile, wireless, and handheld computing in the gas, electric, cable, and telephone industries, the conference agenda addresses field services, work management and field inspection, mobile GIS and GPS, and cost justification.

The \$995 registration fee covers all three days, including tutorials, reception, lunches, the banquet, and a copy of the directory. To register or to get more information, contact PDA Inc. (San Francisco) at 415.252.8008 or visit www.pda-expo.com.

■ **Hot Chips, Interconnects at The Farm**

The annual **Hot Chips** and **Hot Interconnects** conferences come to Stanford University August 21-26. The first three days cover network interfaces, optical and electrical signaling, and IPv6. The next three start with a pair of seminars and finish up with presentations that include Intel's 440LX chip set, several graphics accelerators, UltraSparc-2i, and Pentium II.

Admission costs \$230 for IEEE members; \$290 for non-members. For more information, contact Dr. Robert G. Stewart (Los Altos, Calif.) at 415.941.6699 or visit www.hotchips.org.

■ **Refresh Your Memory Knowledge**

Steven Przybylski, industry consultant and *Microprocessor Report* contributor, is holding two seminars on **DRAM technology**. The first covers both graphics buffers and main memory, including Rambus DRAM, SDRAM, and interactions between memories and system architecture. The second seminar focuses on embedded memories, including merged DRAM and logic.

For more information, contact the Verdande Group (San Jose, Calif.) at 408.984.2719 or access www.verdande.com.

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