# RESOURCES

## Don't Read the Book; See the Show

Technical publisher and itinerant trainer Annabooks is holding a series of three **PCI and USB design workshops** in Austin (Texas) and Milpitas (Calif.). Classes are taught by authors and/or engineers. The two-day PCI Design Workshop is followed by a one-day CompactPCI class and a two-day USB tutorial. The Austin engagement runs December 8–12. The Milpitas location offers only the USB class, October 20–21.

Fees for advance registration (15 days prior to class) range from \$495 to \$995, depending on the course(s) attended. To register, or for more information, contact Annabooks (San Diego) at 619.673.0870 or visit *www.annabooks.com.* 

### Dataquest Dives Into Chip Data

For two days in October, Dataquest will be hosting a conference devoted to semiconductor technology and markets. **Semiconductors '97** happens October 22–23 at the Loews Coronado Bay Hotel near San Diego and features speakers from the marketresearch firm as well as representatives from 3Com, Motorola, TI, Mentor, Microsoft, and Sematech. Two parallel tracks address technology vs. market and strategy issues.

Registration runs \$1,295 for Dataquest clients; \$1,495 for nonclients. To register, or for more information, contact Dataquest (Santa Clara, Calif.) at 805.298.3262 or go on line at *www.dataquest.com*.

### MICRODESIGN Resources

# **Microprocessor Forum** October 13–16, Fairmont Hotel, San Jose, Calif.

Don't miss your chance to attend this year's Forum. This event looks to be a sellout, so please register in advance. Your colleagues are registering at a record rate to hear:

- Intel and Hewlett-Packard's first technical disclosure of the IA-64 64-bit instruction set. Immediately afterward, a panel of key workstation and server vendors will discuss the role of IA-64 in their product roadmaps.
- Rambus's disclosure of the specifications for Direct Rambus DRAM.
- The first public report on Sun's MicroJava.
- Disclosures of new x86 processors from AMD, Cyrix, and IDT/Centaur.
- Thoughts on Computer Architecture for the Future, a presentation by Joel Birnbaum, Hewlett-Packard VP of research and development.

For more details and to register, visit our Web site (*www.MDRonline.com*) or call 707.824.4001.

# San Diego the Hot Spot for Semiconductors

Gorham/Intertech Consulting has chosen sunny San Diego for the site of its three-day conference, **Gallium Arsenide and Other Compound Semiconductors.** Running November 12–14, the conference covers the economics of GaAs, trends in optoelectronics, growing a III–V manufacturing process, silicon-germanium technology, and other topics.

The conference costs \$1,145 per person, with discounts for three or more. To register, or for more information, contact G/IC (Gorham, Maine) at 207.892.5445, fax 207.892.2210.

### What Are the Disk/Trends?

The focused market-research elves at Disk/Trend have prepared four research reports on different aspects of the rotatingmemory market. Priced from \$1,845 to \$2,740 apiece, the reports cover rigid disk drives, optical disk drives, removable storage, and disk-drive arrays. Discounts are available for multiple copies purchased at once.

To order, contact Disk/Trend (Mountain View, Calif.) at 650.961.6209 or visit *www.disktrend.com.* 

### IMAPS/ISHM '97 in Philadelphia

More than 300 companies will be displaying their wares for microelectronics and electronics packaging October 12–16. For more information, call 888.464.6277 or e-mail *IMAPS@aol.com*.

### SUBSCRIPTION INFORMATION

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