AT A OLANOL
Mendocino Improves Celeron
Editorial: Intel Tries Integrating Graphics
Most Significant Bits
Embedded News
In its second major semiconductor innovation within a year's time (the first being copper interconnect), IBM is bringing silicon-on-insulator technology to mainstream microprocessors. The new technology can boost microprocessor speeds by up to 35% over conventional bulk-CMOS processes, or it can reduce their power consumption by up to 65% at the same speed, for only a small additional cost. The new process puts IBM at least a year or two ahead of the rest of the semiconductor industry. The company will apply the technology first to its PowerPC processors beginning early next year.
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The Slater Perspective and Chart Watch will return next issue.

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