

### ■ Embedded Systems Ready for a Fall

Every fall the **Embedded Systems Conference** comes to the San Jose (Calif.) Convention Center, and this year is no exception. Held November 2–5, the four-day conference includes multiple tracks filled with 90-minute presentations and a large exhibit hall. As in previous years, talks will cover software tools, operating systems, programming languages, debugging, and development. Among the speakers will be *Microprocessor Report's* Jim Turley pontificating on microprocessors for embedded applications. Jim will also host ESC's first-ever panel discussion, entitled "Silicon Volley—Windows CE: Hype or Hope?"

Registration costs vary from free (exhibits only) to \$1,645 for a four-day pass plus one all-day workshop on Sunday. For more information, or to register, contact Miller Freeman (San Francisco) at 800.789.2223 or visit [www.embedded.com](http://www.embedded.com).

### ■ An Insider's View of Intel

*Creating the Digital Future: The Secrets of Consistent Innovation at Intel*, by Albert Yu, offers the most intimate look ever at the evolution of the microprocessor industry from Intel's perspective. Mr. Yu has been with Intel since 1972 (except for a brief excursion to found a very early PC startup, VideoBrain) and is currently senior VP and general manager of Intel's Microprocessor Products Group. More about Intel than about the digital future, the book starts with familiar ground like Moore's Law

and the 1979 "Crush" campaign but quickly moves on to explore the birth of MMX, how Intel drives the PC industry, the rise and fall of the RISC challenge, oddities such as the '432 and '860, and Intel's current product strategy—as well as a few management topics, such as how to train general managers.

Published by Simon and Shuster ([www.simonsays.com](http://www.simonsays.com)), the book lists for \$27.50.

### ■ Parlez Vous Programming?

The IEEE's annual conference on **Parallel Architectures and Compilation Techniques** (PACT '98) will be held at the Ecole Nationale Supérieure des Télécommunications in Paris (France). The five-day conference, which sadly conflicts with the Microprocessor Forum October 13–17, will include addresses by Gordon Bell, Maurice Wilkes, and Carole Dulong, principal engineer and IA-64 architect at Intel. Presentations will include "Scanning Polyhedra Without Do Loops," "Exploiting Fine- and Coarse-Grained Parallelism in Embedded Programs," and "Optimistic Register Coalescing," among others. Tuesday's program ends with the obligatory wine and cheese tasting.

Registration costs about \$560 for members; \$692 for non-members. Single-day workshops on reconfigurable computing and directed compilation are free of charge but require advance space reservations. For more information, or to register, visit [www.cs.njit.edu/pact](http://www.cs.njit.edu/pact).