

# DS21S07A SCSI Terminator

### FEATURES

- Fully compliant with SCSI-1, Fast SCSI and Ultra SCSI
- Backward compatible to the DS2107 and DS2107A
- · Provides active termination for nine signal lines
- Laser-trimmed 110 ohm termination resistors have 2% tolerance
- Low dropout voltage regulator
- Power-down mode isolates termination resistors from the bus
- SCSI bus hot plug-compatible
- Fully supports actively negated SCSI signals
- Onboard thermal shutdown circuitry
- 16-pin plastic SOIC (DS21S07AS) and 20-pin plastic TSSOP (Thin Shrink Small Outline Package) (DS21S07AE)

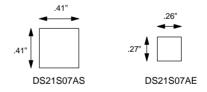
### **PIN ASSIGNMENT**

TERMPWR1		1	$\bigcirc$	16		PD
R1		2		15		VREF2
R2		3		14		NC
R3		4		13		R9
R4		5		12		R8
R5		6		11		R7
VREF1		7		10		R6
GND		8		9		TERMPWR2
	DS21	S07A	S 16–PIN	SOI	C (30	00 MIL)
TERMPWR1		1	$\bigcirc$	20		PD
HS-GND		2		19		VREF2
R1		3		18		HS-GND
R2		4		17		NC
R3		5		16		R9

R4 🕮	6	15 🞞	] R8
R5 💷	7	14 🖽	] R7
HS-GND	8	13 🎞	] R6
VREF1	9	12 🎞	∃ HS-GND
GND 🞞	10	11 🗖	TERMPWR2

DS21S07AE 20-PIN TSSOP (173 MIL)

### ACTUAL FOOTPRINT SIZE



### DESCRIPTION

Fast SCSI and Ultra SCSI require the use of active terminations at both ends of every cable segment in a SCSI system with single–ended drivers and receivers. The DS21S07A SCSI Terminator, which is fully compliant with these standards, enables the designer to gain the benefits of active termination: greater immunity to voltage drops on the TERMPWR (TERMination PoWeR) line, enhanced high-level noise immunity, intrinsic TERMPWR decoupling, and very low quiescent current consumption. The DS21S07A integrates a regulator and nine precise switched 110 ohm termination resistors into a monolithic IC. The DS21S07A can be electrically isolated from the SCSI bus without physical removal from the SCSI device. DS21S07A

### **FUNCTIONAL DESCRIPTION**

The DS21S07A consists of a bandgap reference, buffer amplifier, and nine termination resistors (Figure 1). The bandgap reference circuit produces a precise 2.55V level which is fed to a buffer amplifier. The buffer produces a 2.85V level and is capable of sourcing at least 24 mA into each of the termination resistors when the signal line is low (active). When the driver for a given signal line turns off, the terminator will pull the signal line to 2.85V (quiescent state). To handle actively negated SCSI signals, the buffer can sink at least 200 mA, and V<sub>RFF</sub> will move less than 60 mV. When all lines settle in the quiescent state, the regulator will consume about 2.5 mA. When the DS21S07A is put into power-down mode by bringing PD low, the power-down circuitry will turn off the transistors on each signal line. This will isolate the DS21S07A from the signal lines and effectively remove it from the circuit. The power-down pin  $(\overline{PD})$ has an internal 50K $\Omega$  pull-up resistor. To place the DS21S07A into an active state, the  $\overline{\text{PD}}$  pin should be left open circuited. When installed on disk drives or RAID system components, the DS21S07A will not affect the SCSI bus during a hot plug operation.

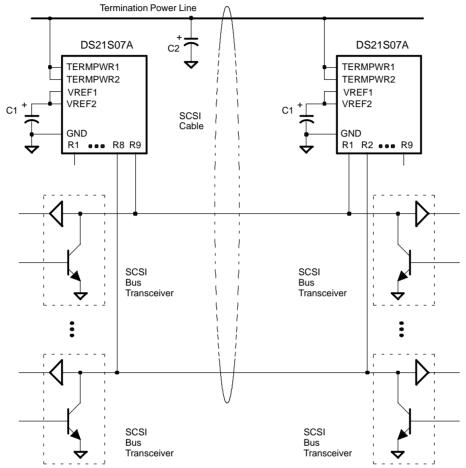
To ensure proper operation, both the TERMPWR1 and TERMPWR2 pins must be connected to the SCSI bus TERMPWR line and both the VREF1 and VREF2 pins must be tied together externally. Each DS21S07A requires a 4.7  $\mu$ F capacitor connected between the VREF pins and ground. Figure 2 details a typical SCSI bus configuration. In an 8–bit wide SCSI bus arrangement ("A" Cable), two DS21S07A's would be needed at each end of the SCSI cable in order to terminate the 18 active signal lines. In a 16–bit wide SCSI bus arrangement ("P" Cable), three DS21S07A's would be needed at each end of the SCSI cable in order to terminate the 27 active signal lines.

### PD TERMPWR1 Power **TERMPWR2** Down 110 Ohms 50K Ohm Circuitry $\sim$ R1 110 Ohms $\sim$ R2 Bandgap 110 Ohms Reference ᠕᠕ R3 110 Ohms ᠕᠕ R4 110 Ohms R5 ᠕᠕ 110 Ohms R6 $\mathcal{M}$ GND 110 Ohms R7 $\sim$ 110 Ohms R8 ∿∿∿ 110 Ohms R9 $\sim$ VREF1 VREF2

### DS21S07A BLOCK DIAGRAM Figure 1

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# TYPICAL SCSI BUS CONFIGURATION Figure 2



### NOTES:

- 1. C1 = 4.7  $\mu$ F tantalum C2 = 2.2  $\mu$ F tantalum or 4.7  $\mu$ F aluminum
- 2. If the DS21S07A is to be embedded into a peripheral that will act as a target on a SCSI bus, it is that recommended TERMPWR be derived from the SCSI cable, not generated locally. In this configuration, if a power failure occurs in the peripheral, it will not affect the bus.
- 3. A high frequency bypass capacitor (0.1  $\mu$ F recommended) can be added in parallel to C1 for applications using fast rise/fall time drivers.

DS21S07A

### PIN DESCRIPTION Table 1

DS21S07AE PIN	DS21S07AS PIN	SYMBOL	DESCRIPTION			
1	1	TERMPWR1	Termination Power 1. Should be connected to the SCSI TERMPWR line. Must be decoupled with either a 2.2 $\mu$ F or 4.7 $\mu$ F. See Figure 2.			
3	2	R1	Signal Termination 1. 110 ohm termination.			
4	3	R2	Signal Termination 2. 110 ohm termination.			
5	4	R3	Signal Termination 3. 110 ohm termination.			
6	5	R4	Signal Termination 4. 110 ohm termination.			
7	6	R5	Signal Termination 5. 110 ohm termination.			
9	7	VREF1	<b>Reference Voltage 1.</b> Must be externally connected directly to the VREF2 pin. Must be decoupled with a 4.7 $\mu$ F capacitor as shown in Figure 2.			
10	8	GND	Ground. Signal ground; 0.0V.			
11	9	TERMPWR2	Termination Power 2. Should be connected to the SCSI TERMPWR line. Must be decoupled with either a 2.2 $\mu$ F or 4.7 $\mu$ F. See Figure 2.			
13	10	R6	Signal Termination 6. 110 ohm termination.			
14	11	R7	Signal Termination 7. 110 ohm termination.			
15	12	R8	Signal Termination 8. 110 ohm termination.			
16	13	R9	Signal Termination 9. 110 ohm termination.			
17	14	NC	No Connect. Do not connect any signal to this pin.			
19	15	VREF2	<b>Reference Voltage 2.</b> Must be externally connected directly to the VREF1 pin. Must be decoupled with a 4.7 $\mu$ F capacitor as shown in Figure 2.			
20	16	PD	<b>Power Down.</b> When tied low, the DS21S07A enters a power- down mode. Contains an internal 50K pull–up. Strap low to deac- tivate the DS21S07A, leave open circuited to activate the DS21S07A.			
2, 8, 12, 18	N/A	HS-GND	Heat Sink Ground. Internally connected to the mounting pad. Should be either grounded or electrically isolated from other cir- cuitry.			

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ABSOLUTE MAXIMUM RATINGS\* Voltage on Any Pin Relative to Ground Operating Temperature Storage Temperature Soldering Temperature

-1.0V to +7.0V 0°C to 70°C -55°C to +125°C 260°C for 10 seconds

\* This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operation sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods of time may affect reliability.

RECOMMENDED OPERATING CONDITIONS (0°C to 70						
PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS	NOTES
TERMPWR Voltage	V <sub>TP</sub>	4.00		5.50	V	
PD Active	V <sub>PDA</sub>	-0.3		+0.8	V	
PD Inactive	V <sub>PDI</sub>	2.0		V <sub>TP</sub> +0.3	V	

### **DC CHARACTERISTICS**

(0°C to 70°C)

PARAMETER	SYMBOL	MIN	ТҮР	MAX	UNITS	NOTES
TERMPWR Current	I <sub>TP</sub> I <sub>TP</sub>		2.5	250 5	mA mA	1, 3 1, 4
Power Down Current	I <sub>PD</sub>		100	150	μA	1, 2, 5
Termination Resistance	R <sub>TERM</sub>	108	110	112	ohms	1, 2
Die Thermal Shutdown	T <sub>SD</sub>	150			°C	1, 6
Power Down Termination Capacitance	C <sub>PD</sub>		3.0	5.0	pF	1, 2, 5, 6
Input Leakage High	I <sub>IH</sub>	-1.0			μA	1, 8
Input Leakage Low	۱ <sub>۱L</sub>			1.0	μA	1, 7

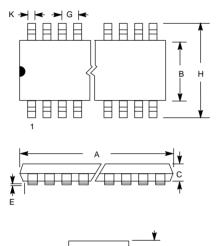
REGULATOR CHARACTERISTICS					<i>,</i> ,	(0°C to 70°C)	
PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS	NOTES	
Output Voltage	V <sub>REF</sub>	2.79	2.85	2.93	V	1, 2	
Drop Out Voltage	V <sub>DROP</sub>		0.50	0.75	V	3, 6	
Line Regulation	LI <sub>REG</sub>		1.0	2.0	%	1, 4	
Load Regulation	LO <sub>REG</sub>		1.3	3.0	%	1, 3	
Current Limit	I <sub>LIM</sub>		350		mA	1	
Sink Current	I <sub>SINK</sub>	200			mA	1	

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### NOTES:

- 1. 4.00V < TERMPWR < 5.50V.
- 2. 0.0V < signal lines < TERMPWR.
- 3. All signal lines = 0.0V.
- 4. All signal lines open.
- 5. <u>PD</u> = 0.0V.
- 6. Guaranteed by design; not production tested.
- 7.  $R_1$  through  $R_9$  only.
- 8.  $R_1$  through  $R_9$  and  $\overline{PD}$ .

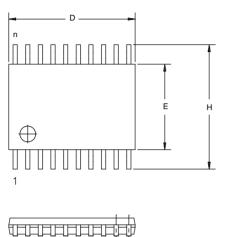
# DS21S07AS SCSI TERMINATOR 16-PIN SOIC (300 MIL)



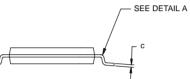
PKG	16-PIN				
DIM	MIN	MAX			
A IN.	0.402	0.412			
MM	10.21	10.46			
B IN.	0.290	0.300			
MM	7.37	7.65			
C IN.	0.089	0.095			
MM	2.26	2.41			
E IN.	0.004	0.012			
MM	0.102	0.30			
F IN.	0.094	0.105			
MM	2.38	2.68			
G IN.	0.050 BSC				
MM	1.27 BSC				
H IN.	0.398	0.416			
MM	10.11	10.57			
J IN.	0.009	0.013			
MM	0.229	0.33			
K IN.	0.013	0.019			
MM	0.33	0.48			
L IN.	0.016 0.04				
MM	0.40 1.02				
phi	0°	8°			

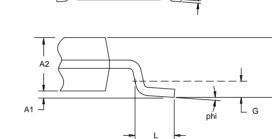
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## DS21S07AE SCSI TERMINATOR 20-PIN TSSOP



e1 🗕





DETAIL A

DIM MIN MAX A MM \_ 1.10 A1 MM \_ 0.05 0.75 A2 MM 1.05 СММ 0.09 0.18 LMM 0.50 0.70 e1 MM 0.65 BSC B MM 0.18 0.30 D MM 6.40 6.90 EMM 4.40 NOM

6.25

0°

0.25 REF

6.55

**8**°

G MM

н мм

phi

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