## **APPLICATION NOTE 021** for PCMCIA Controllers



From: FK Product: MSMJ104/ all PCMCIA interfaces with

VG468

To:Customer/SupportVersion:V?Date:08.96BIOS:V?

## Problem with booting from a PCMCIA cards or accessing I/O-Cards:

Problem: The bootsequence of from a PCMCIA card may be very slow. We know cases

with duration of several minutes, until the operating system comes up.

Impact: The problem is VG486 and the new SCM-software version specific.

With older SCM driver version this problem is never observed.

Two cases are known:

1. the READY signals of the PCMCIA must be pulled up with 10k.

2. the reference clock of the VG468/465 must be free of capacitive loads.

## Solution:

- Check that the pullup's are connected to the READY pin (Pin 16 RDY / Pin 17 Vcc).
  On Digital-Logic products this issue is solved since april 1996.
  On other product this resistor must be added between Pin 16 and Pin 17.
- 2. Check the BUSCLK (8 / 10 / 12.5Mhz) and the ATCLK (14.3Mhz) lines on the bus or buspanel. Both lines must be free of capacitors or other load.

REMARK: MSMJ104 uses the BUSCLK as reference clock!

Longterm Solution:

none