



# Am186™ER and Am188™ER Microcontrollers

## User's Manual

This document amends the *Am186™ER and Am188™ER Microcontrollers User's Manual* (order #21684B).

This amendment consists of one documentation change affecting the hardware watchdog timer on reset. The documentation should state the following:

- The Am186ER and Am188ER hardware watchdog timer is *inactive* after reset.

Table 1 lists the documentation changes.

**Table 1. Corrections to the *Am186™ER and Am188™ER Microcontrollers User's Manual*, Rev. B**

Subheading	Page	Original Text	Change To
8.1 OVERVIEW	8-1	The WDT is active after reset.	The WDT is inactive after reset.
	8-1	After reset, the WDT is enabled and the timeout period is set to its maximum value.	[Delete sentence.]
8.1.1 Watchdog Timer Control Register (WDTCON, Offset E6h)	8-1	The watchdog timer is enabled out of reset and configured to system reset mode with a maximum timeout count.	[Delete sentence.]
	8-1	<b>Note:</b> <i>The Watchdog Timer (WDT) is active after reset.</i>	<b>Note:</b> <i>The Am186ER and Am188ER hardware watchdog timer is inactive after reset.</i>
	8-2	The value of the WDTCON Register at reset is C080h.	The value of the WDTCON Register at reset is 4080h.
	8-2	This bit is 1 after processor reset. [This sentence is in the <b>Bit 15: Watchdog Timer Enable (ENA)</b> description.]	This bit is 0 after processor reset.