Literature Watch

Buses

VMEbus SSBLT spec now on target. Warren Andrews; Computer Design, 6/92, pg 48, 2 pgs.

Development Tools

Integrated board-layout software sports low price for high-end capabilities. Don Hargrave, Zyrel Inc.; Pers. Eng. & Inst. News, 6/92, pg 59, 4 pgs.

PC-based CAE/CAD: any task, anywhere, anytime. *The PC is today's most popular engineering platform. Portable, powerful, fast, and inexpensive, it's a match for workstations in all but the most demanding CAE/CAD applications.* Jon Gabay; Computer Design, 6/92, pg 83, 9 pgs.

PC-based design software: schematic capture and pc-board layout on $1600. *You can get a surprising amount of utility from low-cost schematic-entry and pc-board-layout software.* Doug Conner; EDN, 5/21/92, pg 63, 6 pgs.

Technologies move toward hardware/software codesign. Barbara Tuck; Computer Design, 6/92, pg 35, 4 pgs.

The 8051 lives long and prospers—thanks to modern tools. Ken Anderson, ChipTools; Pers. Eng. & Inst. News, 6/92, pg 65, 3 pgs.

VHDL: a new reality in digital design. Steve Carlson, Srinivas Raghvendra, and Deirdre Hanford, Synopsys Inc.; Electronic Products, 6/92, pg 31, 6 pgs.

Graphics

Graphics ICs target Windows acceleration. Dave Wilson; Computer Design, 6/92, pg 71, 6 pgs.

Memory

Associative processors and memories: a survey. *Because of declining hardware prices, associative architectures are again gaining importance, especially in artificial intelligence and database*

applications. Karl E. Grosspietsch, German National Res. Ctr. for Comp. Sci.; IEEE Micro, 6/92, pg 12, 8 pgs.

Cascading content-addressable memories. Tim Moors, Antonio Cantoni, University of Western Australia; IEEE Micro, 6/92, pg 56, 11 pgs.

Setting the record straight on SRAM loading. Dave Wilson; Computer Design, 6/92, pg 32, 2 pgs.

The future is in the PC cards. *Originally designed for harsh environments, these compact cards have evolved into a standard for handheld systems.* Daniel Sternglass, Databook, Inc.; IEEE Spectrum, 6/92, pg 46, 5 pgs.

Miscellaneous

ASIC core brings fuzzy logic to semiconductors. Tom Williams; Computer Design, 6/92, pg 28, 2 pgs.

BiCMOS process advances deliver bipolar speed. *As BiCMOS performance improves, the breadth of applications expands from memory and bus drivers to ASICs and CPUs.* Dave Bursky; Electronic Design, 5/28/92, pg 43, 7 pgs.

Low-voltage ICs: 3V circuits cut power and boost speed. Dave Pryce; EDN, 6/4/92, pg 73, 4 pgs.

No accolades for Accolade court. Richard H. Stern, Oblon, Spivak, McClelland, et al.; IEEE Micro, 6/92, pg 3, 4 pgs.

Peripheral interfaces offer fast networking solutions. *HiPPI and Fiber Channel may have been defined as peripheral interfaces, but designers are taking advantage of their high data transfer rates to solve their networking problems.* Warren Andrews; Computer Design, 6/92, pg 59, 6 pgs.

What's real in real-time operating systems? Jeffrey Child; Computer Design, 6/92, pg 107, 8 pgs.

Processors

DSP evaluation kits: learn to use DSP chips with a minimum of pain. *The architectural diversity of DSP uPs makes them unusually tough to use. Evaluation kits ease your educational burden by giving you an operational piece of hardware and the software you need to make the hardware do something useful.* Steven H. Leibson; EDN, 6/4/92, pg 45, 5 pgs.

Low density MCU market remains robust, though less profitable. *The electronification of just about everything keeps demand for 4-bit and 8-bit devices soaring.* Hugh G. Willett; Electronic Business, 6/92, pg 145, 4 pgs.

Superscalar SPARC RISC integrates CPUs, cache controller, and SRAM. Ray Weiss; EDN, 6/4/92, pg 92, 2 pgs.

Superscalar SPARC executes as many as three instructions in parallel. Ray Weiss; EDN, 6/4/92, pg 89, 3 pgs.

Programmable Logic

Application-tailored PLDs streamline designs, bring speed and lower cost. *PLDs tailored for specific applications offer many performance advantages. The question is, do you want to learn a new architecture?* Richard A. Quinnell; EDN, 5/21/92, pg 81, 6 pgs.

Cyrus Y. Tsui on: programmable logic. Cyrus Y. Tsui, Lattice Semiconductor Corp.; Computer Design, 6/92, pg 23, 3 pgs.

FPGA conversion. *Knowing that you could convert a field-programmable gate array to a mask-programmed device may be reassuring, but it doesn't guarantee that conversion will happen.* Charles H. Small; EDN, 6/4/92, pg 107, 8 pgs.