

At A Glance

MIPS Reaches New Lows With R4200 Design	1
The R4200 CPU, developed by MIPS Technologies and NEC, has a smaller die size than a 486DX and is expected to cost about \$70 in volume. The chip offers nearly twice the performance of the fastest 486 and can be used in low-cost Windows NT systems.	
Editorial: API Wars—Too Late for Apple?	3
Several vendors are developing products to offer the Windows API on multiple platforms, but Apple continues to resist spreading its Macintosh operating system, a tactic that could spell trouble for Apple.	
Most Significant Bits	4
Pentium debuts at \$965—systems available; Cypress exits SPARC chip business; Apple demonstrates PowerPC Macintosh—at 80 MHz; MIPS' T5 moves ahead, but TFP slips out; Windows NT slated for July release...as several vendors announce RISC PCs; Star-studded NeTpower to build NT systems; Intel files ITC complaint against Twinhead; TI licenses ARM for embedded control.	
AMD Upgrades Performance of 29200	10
The new 29240 adds an on-chip cache and higher clock speeds to the 29200 family, improving performance by up to four times.	
Vitesse Unveils Pentium Cache Controller	12
A new gallium-arsenide cache controller provides zero-wait-state performance for Pentium systems without breaking the bank.	
Cyrix Delivers Revamped M6 Processors	14
Cyrix extends its 486 product line with a range of parts that include power-management features and a 3.3V option.	
IC Manufacturing Drives CPU Performance	15
The process of building an integrated circuit helps determine the cost and performance of that chip, particularly for processors.	
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RISC processor vendors see Windows NT as the road to increased sales, but a product called Chicago may dash these hopes.	
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