

Resources

IC Card Expo in Silicon Valley

The second annual *IC Card Expo* will be held July 27 and 28 at the Santa Clara (CA) Convention Center. The show covers PC cards and related products. This year's technical track features hot topics such as PCMCIA, Rambus, and flash memory, as well as other hardware, software, and manufacturing issues. The management track covers market trends and application areas. Two seminars will be held in the mornings, concurrent with the main conference; one will discuss PCMCIA software, and the other will focus on packaging and manufacturing technologies. There will also be a vendor exhibition.

Advance registration (by 7/12) costs \$250 for a two-day pass or \$150 for a single day. Registration for the seminars costs \$260 for one seminar and a one-day pass to the conference, or \$350 for both seminars and a two-day pass. Contact IC Card Expo at 6300 S. Syracuse Way, Suite 650, Englewood, CO 80111; 800/525-9154 or 303/220-0600, fax 303/770-0253.

Learn About PC Design from Annabooks

Annabooks is offering a three-day course, *PC Design for Embedded Applications*, on three separate occasions. In San Diego, the course will be held June 23–25 and again on November 10–12. The course will also be given in Boston on September 27–29. It covers topics such as ISA and EISA, PC chip sets, ROM-based DOS and applications, and flash memory. The sessions will be taught by Annabooks authors and other industry experts. Tuition is \$1495, with a \$200 discount for registration 45 days in advance.

Annabooks sells a variety of books for PC designers, such as *ISA & EISA Theory of Operation*, *PC Keyboard Design*, and *Hard Drive Encyclopedia*. The company also sells BIOS kits, developer's kits, and other development

tools. Ask them for a free catalog.

Contact Annabooks at 15010 Avenue of Science #101, San Diego, CA 92128; 800/462-1042 or 619/673-0870, fax 617/673-1432.

Compiler Workshops in Oregon

The Oregon Graduate Institute of Science and Technology (OGI) will offer two advanced workshops on designing high-performance compilers. *Optimizing for Modern Architectures* will be held August 16–20, followed by *Optimizing for Parallelism* on August 23–27, both in Portland. The courses will be presented by Dr. Michael Wolfe, a co-founder of Kuck and Associates. The courses are intended for compiler designers as well as processor designers who want to understand these techniques to optimize the performance of their systems.

Tuition is \$1595 per course or \$2895 for both. Early registration is recommended. Contact OGI at 19600 NW von Neumann Drive, Beaverton, OR 97006; 503/690-1686, fax 503/690-1219, or e-mail lpease@admin.ogi.edu.

Electronic Market Data Book

Published for over 35 years, the *Electronic Market Data Book* provides a statistical overview of the electronics industry, including consumer electronics, telecommunications, computers and peripherals, components, and defense. Developed by the Electronics Industry Association (EIA), the book focuses on the US but includes a section on international opportunities as well. The 1993 version costs \$75 for EIA members, \$125 for non-members.

For continuous coverage, the EIA also has a monthly publication called *Electronic Market Trends*; a one-year subscription costs \$65 for members and \$75 for non-members if purchased with the book. Contact the EIA, Department 287, Washington, DC 20055; 202/457-4950.