

# At a Glance

<b>Cyrix Describes Pentium Competitor</b> .....	1
Cyrix's M1 is a superscalar, deeply pipelined x86 processor that removes many of the bottlenecks of traditional x86 processors. The M1, which is not expected to reach production until 2H94, promises to offer better performance per MHz than Pentium, particularly on 386 and 486 code that has not been recompiled specifically for Pentium.	
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MicroDesign Resources has launched a new newsletter, <i>MicroSystems Insider</i> , and named Linley Gwennap as the new Editor in Chief of <i>Microprocessor Report</i> . Michael Slater will serve as Publisher and Editorial Director of both publications.	
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Digital announces world's fastest CPUs; NexGen quietly samples 586, at last; Orion announced as R4600; DeskStation launches MIPS chip set; Intel makes major pricing cuts; Hitachi outlines roadmap for SH7000.	
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The 603 is the first PowerPC chip designed from scratch. It delivers performance a bit slower than the 601 (and comparable to Pentium) but its power consumption is suitable for notebook systems. Both IBM and Motorola will build and market the chip, which should be available in 3Q94.	
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Three new chip sets from AT&T offer a range of price/performance options for Hobbit-based systems. The new processors meet or exceed the performance of all other 3.3-V PDA processors and hold a large advantage over Intel/VLSI's new Polar processor.	
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The Am386SC is the first chip to combine a 386 core with all the system logic and peripheral interfaces needed for a DOS-compatible handheld PC. AMD is also planning to customize the design for specific applications.	
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Intel has belatedly developed a set of 386 chips specifically for the embedded market, offering an upgrade path to users of the popular 80186. The chips feature static, low-voltage operation; the 386EX will also include a set of on-chip peripherals.	
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The V800 architecture is similar to standard RISC but a few twists make it more attractive for embedded and PDA designs. The latest implementation, the V820, combines a V810 core with system logic to simplify embedded designs.	
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*Microprocessor Report* is published every three weeks, 17 issues per year. Rates are: *N. America*: \$445 per year, \$795 for two years. *Europe*: £345 per year, £595 for two years. *Elsewhere*: \$575 per year, \$995 for two years. Additional copies in the same envelope: \$145 per year in North America, \$195 elsewhere. Back issues are available.

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**Published by**  
**MICRODESIGN**

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