# Resources

## Designing Windows Systems

Join other designers of Windows PCs at Microsoft's third **Windows Hardware Engineering Conference** (WinHEC), to be held February 23–25 at Moscone Center in San Francisco. The conference features sessions on Microsoft's system software strategy and its impact on hardware design. Bill Gates will give a keynote presentation on Microsoft's vision for the future of PCs.

"Technology Forums" will cover a range of topics including displays, networking, servers, video, telephony, memory and cache subsystems, mobile systems, and peripheral devices. Our own Michael Slater will give a presentation on hardware technology and system design trends, as well as a separate half-day seminar on microprocessors for Windows systems. Other seminars cover device drivers for "Chicago," device drivers for Windows NT, and the Plug-and-Play architecture. A vendor exhibition will provide an opportunity to a variety of hardware technologies and meet with the suppliers.

Early registration (by 1/23) is $595; seminar registration (for any two half-day seminars) is $250 for conference attendees or $500 if attending only the seminars. Exhibits-only registration is $50. Call 800.421.6338 or 415.543.5847 to register or request more information.

## ISSCC Features New Microprocessors

Unlike the past couple of years, some important new microprocessors will debut at **International Solid-State Circuits Conference** (ISSCC), to be held February 16–18 at the San Francisco Marriott. Thursday's microprocessor session includes a paper on Intel's P54C, the 100-MHz version of Pentium, and HP's PA-7200, the next-generation PA-RISC chip. Other papers describe MIPS Technologies' TFP and R4200 chips as well as the PowerPC 603. That evening, a panel featuring our own Nick Tredennick along with representatives from several processor vendors will discuss the impact of Windows NT on the CPU market. The remainder of the conference focuses on new technologies for memory, logic chips, analog design, packaging, and other IC topics.

Early registration (before 2/1) costs $270, or $250 for IEEE members. Contact ISSCC at 415.327.6622, fax 415.321.9692.

## Nomads Gather in Santa Clara

A conference for the designers of portable, nomadic, and mobile devices, *Electronic Design* magazine's **Portable By Design**, will be held on February 14–18 at the Santa Clara (Calif.) Convention Center. Monday's program consists of workshops, while the rest of the week features more than 60 technical presentations, ranging from microprocessors and operating systems to power management and storage devices. A second conference, the **Wireless Symposium**, will be held concurrently. Both shows include vendor exhibitions.

Attendance at either conference costs $425, which includes limited access to the other conference. Workshops run an additional $275 for the day. For more information, contact Betsy Tapp of *Electronic Design* at 201.393.6075, fax 201.393.0204.

## Winning the ARM Race

Addison-Wesley has published a new book, ***The ARM RISC Chip—A Programmer's Guide***, by Alex van Someren and Carol Atack in collaboration with ARM, Ltd. The book is intended for hardware designers as well as programmers and covers the complete ARM instruction set along with the ARM6 CPU, which is used in the Apple Newton and other products. The paperback book is listed as ISBN 0-201-62410-9 and carries a US list price of $34.95 (or £24.95 in the UK). Contact Addison-Wesley at 800.447.2226.