

## Resources

### Marooned on an Island with Engineers

The 1995 **Parallel Architectures and Compilation Techniques** conference (PACT '95) will be held in the city of Limassol, Cypress. The conference runs for four days, June 26–29, in the Limassol Sheraton on the southern shore of the island. The organizers hope to promote a dialogue between engineers and compiler writers that will enhance and exploit more efficient parallelism.

Conference registration for all four days costs \$385 for ACM/IEEE members or \$435 for nonmembers. For more information, or to register, consult the ACM WWW page at <http://acal-www.usc.edu/pact.pact.html> or fax the ACM (Newark, N.J.) at 201.596.5777.

### ¡Hablamos Supercomputers!

In the week immediately following PACT '95, the ACM is sponsoring its ninth annual **International Conference on Supercomputing** in Barcelona, Spain. This year's gathering will focus on the architecture, applications, and software of high-performance and parallel computing systems. Held July 3–7, the conference can be found at the Barcelona Hilton.

Registration runs \$540 for ACM members or \$645 for nonmembers. To register, contact the ACM (Portland, Oregon) at 503.578.5530; fax 503.578.3228 or via the WWW at <http://www.cse.ogi.edu/ICS>.

### The Microprocessor Is History

With funding from industry and academia, one Silicon Valley luminary is videotaping the history of the valley's most famous produce. Rob Walker, a founder of LSI Logic and UC Berkeley alumnus, began the **Silicon Valley Genesis Project** earlier this year, recording interviews with the industry's early pioneers. Those interviewed so far include Fairchild CEO and Motorola GM C. Lester Hogan; John Pierce, who coined the word tran-

sistor while at Bell Labs; and microprocessor developer Ted Hoff. The completed series will contain more than 25 additional interviews. The video tape series will eventually be digitized for transmission over the Internet.

Copies of the series will be housed at the Department of Special Collections, Stanford University Libraries, in Palo Alto (Calif.). For information, contact Henry Lowood, curator, at 415.723.4602.

### Stanford University Now Open to All

The Western Institute of Computer Science at Stanford University is offering 14 intensive short courses on computer design from July 17 through August 25. Subjects include computer system performance modeling, distributed computing, transaction processing, and parallel architectures, among others.

Early-bird tuition (14 days before the course starts) is \$1,440; regular tuition runs \$1,575, with \$100 discounts for multiple courses or referrals. For more information, or to register, contact WICS (Magalia, Calif.) at 916.873.0575; fax 916.873.6697; or access the WICS Web page at <http://www-wics.stanford.edu/WICS.html>.

### Chip Company Profiles Updated for 1995

The latest edition of Integrated Circuit Engineering's **Profiles 1995** has just been published. The report includes a worldwide survey of nearly 200 IC manufacturers, including 10 new additions to last year's edition. The 550-page publication includes company overviews, financial histories, process capabilities, information on fab locations and equipment, and company alliances. The survey is available in printed form or on CD-ROM.

Initial copies are priced at \$995, or \$1,045 for customers outside the U.S. Additional copies are \$395 and \$465. For more information, contact ICE (Scottsdale, Ariz.) at 602.998.9780; fax 602.948.1925.