## Recent IC Announcements

| Part Number | Vendor | Description | Price/Quantity | Availability |
| :---: | :---: | :---: | :---: | :---: |
| DSP |  |  |  |  |
| D950 | $\begin{aligned} & \text { SGS-Thomson } \\ & 617.259 .0300 \end{aligned}$ | Fixed-point 16-bit DSP core has dual 40-bit accumulators and barrel shifters with coprocessor interface; in 0.5-micron three-layer-metal. | \$10/10,000 | Prod.-Now |
| Interface |  |  |  |  |
| AIT1108E | AITech 800.882.8184 | Chip converts computer-generated RGB video to NTSC or PAL television output and S-VHS; in PLCC-84 or PQFP-100. | \$59/10,000 | Prod.-Now |
| ML6401 | Micro Linear $408.433 .5200$ | High-speed 8-bit A/D converter produces 20 million samples/s with 7 bits effective resolution; in 24 -lead SOIC package. | \$6.75/1,000 | Samples-Now <br> Prod.-9/95 |
| SMC91C94 | $\begin{aligned} & \text { SMC } \\ & 516.273 .3100 \end{aligned}$ | Ethernet controller for PCMCIA cards has 4,608 bytes on-chip RAM, PCMCIA and ISA interface logic, and 10BASE-T compatibility. | \$15/1,000 | Prod.-Now |
| 21052 | $\begin{aligned} & \text { Digital } \\ & 800.332 .2717 \end{aligned}$ | Second-generation PCI bridge chip offers lower power consumption and pin count than 21050 , is compliant with PCl revision 2.0. | \$16.90/5,000 | Prod.-Now |
| Memory |  |  |  |  |
| DIMM-256 | Smart Modular $510.623 .1231$ | Dual-inline memory modules (DIMM) for Pentium L2 cache are available in 256 K to 1 M capacities for $50-66-\mathrm{MHz}$ systems. | \$80/1,000 | Prod.-Now |
| LH5332600N | Sharp 800.642.0261 | Masked ROM has 32 -Mbit capacity and fast 100-ns access time; organized as $4 \mathrm{M} \times 8$ or $2 \mathrm{M} \times 16$; in 0.6 -micron $5-\mathrm{V}$ CMOS. | \$30/1,000 | Samples-Now <br> Prod.-3Q95 |
| Miscellaneous |  |  |  |  |
| bq2050 | Benchmarq 214.437.9195 | Battery-charge indicator IC is designed for lithium-ion batteries; provides both milliamp-hours and milliwatt-hours indication. | \$4.84/10,000 | Samples-Now Prod.-8/95 |
| bq2004E | Benchmarq 214.437.9195 | Charging manager works with NiCd, NiMH, and lithium-ion batteries; regulates voltage within 1\%; in 16-lead, 150 -mil SOIC package. | \$3.70/10,000 | Prod.-Now |
| $\begin{aligned} & \text { ICX074AL-6 } \\ & \text { ICX074AK-6 } \end{aligned}$ | Sony <br> 800.288.7669 | Black-and-white ('AL) and color ('AK) CCD image sensors have square half-inch arrays, can process images at up to 60 fps . | \$260/1,000 | Prod.-Now |
| ADM70x | Analog Devices $617.937 .1428$ | Microprocessor supervisory circuits are available for both 5-V and 3.3-V CPUs, with active high or low reset pulse, individual thresholds. | \$0.97/1,000 | Prod.-Now |
| Peripheral |  |  |  |  |
| CL-FC1120 | Cirrus Logic $510.623 .8300$ | Single-chip controller for ATA Flash memory cards combines 80C51 microcontroller with PCMCIA control logic and interface circuitry. | \$35/1,000 | Samples-Now <br> Prod.-3Q95 |
| FDC37C669 | $\begin{aligned} & \text { SMC } \\ & 516.273 .3100 \end{aligned}$ | Floppy-disk controller is Plug-and-Play compatible and includes IDE, enhanced parallel port, and two UARTs. In PQFP-100 package. | \$7/1,000 | Samples-Now <br> Prod.-3Q95 |
| Programmable Logic |  |  |  |  |
| MACH231-7 | AMD 800.222.9२23 | CPLD has 128 macrocells with fixed 7.5-ns pin-to-pin delay; macrocells have power-down capability for low-power operation; in PLCC-84. | \$60.46/1,000 | Prod.-Now |
| EPM9320 | Altera $408.894 .7000$ | EPLD has 320 macrocells and offers in-system programmability and JTAG boundary scan; with 15 - and $20-\mathrm{ns} \mathrm{T}_{\mathrm{PD}}$; in 84-lead PLCC. | \$119/100 | Prod.-Now |
| AT17C65 <br> AT17C256 | Atmel $408.441 .0311$ | Erasable EPROMs for configuring Xilinx and AT\&T FPGAs come in 128-Kbit and 256-Kbit capacities; in DIP-8 and SOIC-20 packages. | \$2.25/10,000 | Prod.-Now |

