

At a Glance

National Pushes 486 into Embedded Market	1
Becoming the first company to design a 486-class processor specifically for the embedded market, National plans to price its 486SXF, which includes a wide range of on-chip peripherals, at just \$25.	
Editorial: Intel Bows to Microsoft, OEM Pressure	3
In the face of massive opposition from Microsoft, Intel is repositioning its NSP technologies and will delay their roll-out to 1996.	
Most Significant Bits	4
Cyrix spills performance data on 5x86, M1; AMD preparing X5; Intel sneaks write-back into DX4; TI announces low-cost MVP; Motorola plans move to 0.35-micron; Digital's Alpha sales growth slows; PA-8000 gains weight; Erratum: SGS-Thomson resurrects Transputer.	
New PowerPCs Aimed at Consumer Devices	9
Motorola announced the first PowerPC chips to integrate peripherals from its 68300 line, while IBM added an MMU to its lineup.	
Motorola Defines ColdFire Core Again	12
Three new ColdFire chips roll out from Motorola but use a different instruction set than the original ColdFire processor.	
First SPEC95 Results Pump Up HP, IBM	14
Initial SPEC95 testing by several vendors shows that processors with strong cache and memory systems benefit the most.	
Multimedia to Shine at Microprocessor Forum	16
The conference will feature more than a dozen new announcements.	
Viewpoint: Going Native!	17
Although several recent x86 CPUs translate x86 instructions into RISC-like operations to achieve high performance, two Cyrix architects argue that executing x86 code natively is simpler and faster.	
Patent Watch	21
Literature Watch	22
Recent IC Announcements	23
Resources	24

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