## RECENTICANNOUNCEMENTS

| PART NUMBER DSP | VENDOR | DESCRIPTION | PRICE/QUANTITY | AVAILABILITY |
| :---: | :---: | :---: | :---: | :---: |
| DSP32C | AT\&T $800.372 .2447$ | New 16- and 20-MHz versions of floating-point DSP include 1,536 words of RAM, parallel port, and DMA controller. | \$49/10,000 | Prod.-Now |
| TMS320LC548 | $\begin{aligned} & \mathrm{TI} \\ & 800.477 .8924 \end{aligned}$ | New 16 -bit fixed-point DSP runs at 66 MHz and has 32 K words of on-chip SRAM, 8-Mword address space, serial ports, and TDM port. | \$33/50,000 | $\begin{aligned} & \text { Samples-3Q96 } \\ & \text { Prod.-4Q95 } \end{aligned}$ |
| INTERFACE |  |  |  |  |
| IDT77105 | $\begin{aligned} & \text { IDT } \\ & 800.345 .7015 \end{aligned}$ | Physical-interface transceiver for 25-Mbps ATM networks combines transmission convergence (TC), physical media-dependent (PMD) layers. | \$11.50/10,000 | Prod.-Now |
| Am79R79 | AMD <br> 800.222.9323 | Ringing SLIC (subscriber line interface circuit) is suitable for fiber, radio, PCS, set-top boxes, and video telephones; in PLCC-32 package. | \$5.95/10,000 | Prod.-Now |
| 21142 | Digital 800.332.2717 | CardBus-compliant Ethernet controller handles 10 - and $100-\mathrm{Mbps}$ data rates; with PCI interface; device is housed in thin TQFP-100 package. | \$22.70/5,000 | Samples-Now Prod.-2Q96 |
| MEMORY |  |  |  |  |
| KM4132G271 | $\begin{aligned} & \text { Samsung } \\ & 408.954 .7000 \end{aligned}$ | Synchronous graphics RAM is organized as $256 \mathrm{~K} \times 32$ and has I/O bandwidth of $332 \mathrm{Mbytes} / \mathrm{s}$ at 83 MHz ; in 100 -lead PQFP. | \$40/1,000 | Prod.-Now |
| $\begin{aligned} & \text { IDT7005 } \\ & \text { IDT7006 } \end{aligned}$ | IDT <br> 800.345 .7015 | Dual-port memories are fastest available in $\times 8$ organization with 15 -ns access time; in $8 \mathrm{~K} \times 8$ ('05) or $16 \mathrm{~K} \times 8$ ('06) organization. | \$24.64/10,000 | Prod.-Now |
| $\begin{aligned} & \text { 93LC76 } \\ & 93 \text { LC86 } \end{aligned}$ | Microchip $602.786 .7200$ | Serial EEPROMs have 8 -Kbit ('76) and 16-Kbit ('86) capacity; both use three-wire serial interface and have selectable $\times 8$ or $\times 16$ organization. | \$0.95/10,000 | Prod.-Now |
| IDT7014 | IDT <br> 800.345 .7015 | Dual-port memory has 4 K capacity, organized as $4 K \times 9$ with fast, 12 -ns access time, allowing $80-\mathrm{MHz}$ operation; in 64-lead TQFP. | \$22/10,000 | Prod.-Now |
| MISCELLANEOUS |  |  |  |  |
| $\begin{aligned} & \text { CXG1008N } \\ & \text { CXG1012N } \end{aligned}$ | $\begin{aligned} & \text { Sony } \\ & 408.955 .6572 \end{aligned}$ | Monolithic SPDT switches operate at 1.9 GHz for RF and PCS designs; dual GaAs JFETs operate from a single 3-V supply; in SSOP-8 packages. | \$5/10,000 | Prod.-Now |
| HV62208PG | Supertex 408.774 .0100 | Flat-panel driver IC has 32 channels each rated to 80 V , capable of 256 gray levels using pulse-width modulation; in QFP-64. | \$15.49/1,000 | Prod.-Now |
| CY2260 | Cypress $408.943 .2600$ | Clock synthesizer/driver for Pentium/PPro systems works with 82440FX and 82430 VX (Triton II) chip sets; generates 14 outputs. | \$3.10/10,000 | Prod.-Now |
| TC7STxxFU | Toshiba 800.879.4963 | TTL logic devices are compatible with both $5-\mathrm{V}$ and $3.3-\mathrm{V} \mathrm{I} / \mathrm{O}$ and are housed in extremely small ( $2.0 \times 2.1 \mathrm{~mm}$ ) packages. | \$0.60/500 | Prod.-Now |
| PROGRAMMABLE LOGIC |  |  |  |  |
| EPX880 | Altera <br> 408.894.7000 | Flash-based EPLD has 80 megacells and 10-ns pin-to-pin logic delays; offers 1,600 usable gates and field reprogrammability; in QFP-132. | \$60/100 | $\begin{aligned} & \text { Samples-Now } \\ & \text { Prod.-2096 } \end{aligned}$ |

