

## RESOURCES

### ■ X Marks the Spot for DSPs

The expanding DSPx conference, in its fifth year, will take place March 11–14 at the San Jose Convention Center. More than 150 sessions are divided into six technology tracks—DSP, ASIC, mixed-signal, microprocessor, EDA, and test/measurement—and eight market tracks—aerospace, automotive, communications, computer, consumer, industrial, medical, and military. There is also an executive management track and a three-day exhibition.

Registration fees range from \$395 for a one-day workshop to \$1,095 for the executive management package. For more information, contact Reed Exhibitions (Norwalk, Conn.) at 203.840.5652; fax 203.840.9852, send e-mail to [dsp@reed-expo.com](mailto:dsp@reed-expo.com), or check the Web at [www.dsp.com:2000](http://www.dsp.com:2000).

### ■ No Bored Designers Here

Learn more about component technology, high-speed board design, and design for manufacturability at the fifth annual PCB Design Conference. The week-long event includes full-day seminars, partial-day classes, and an exhibition. Joe Costello of Cadence gives the keynote address. The event runs March 18–22 at the Santa Clara (Calif.) Convention Center.

Registration costs \$1,095 for the full week or \$295 per full-day seminar. For more information, contact Miller Freeman (San Francisco) at 415.905.4994; fax 415.905.2220.

### ■ Embedded Designers Huddle in Boston

Topics from real-time operating systems to embedded RISC processors will be covered at the Embedded Systems Conference East on April 2–4 in Boston. Tuesday features a set of five all-day tutorials, while the following two days include a conference that consists of several parallel tracks of short classes. A total of 68 sessions are available for engineers and designers of embedded devices. There is also an exhibition all three days.

Early registration (before 3/19) goes for \$795 for the two-day conference, \$395 for a one-day tutorial, or \$1,095 for all three days. Exhibit passes are free before 3/19. To register, contact ARI (Carrollton, Texas) at 214.242.4262; fax 214.466.4611.

### ■ SPEC Results, DSP Guide on the Web

The Standard Performance Evaluation Committee (SPEC) has put its performance results on the Web. All 1995 benchmarks, including SPEC95, SDM, and SFS, will be posted to the Web as scores are published by SPEC. In fact, the entire SPEC newsletter is also available at the site, [www.specbench.org](http://www.specbench.org). The newsletter is still available via hardcopy for a subscription price.

Berkeley Design Technology (BDT) has launched a Web site focused on DSP technology. The site includes a downloadable version of the company's "Pocket Guide to DSP Processors and Cores" as well as information on BDT's other publications. Check it out at [www.bdti.com](http://www.bdti.com).

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## PC Tech Forum

*Meeting the Challenge of  
New PC Technologies*

Think change can't get any faster in the PC business? Think again. A new set of technologies is emerging to create high-powered, super-connected, graphics-enhanced consumer and business PCs. Find how the new technologies and market trends will change the shape of PCs.

The Forum features a conference on May 22 with speakers from leading PC technology companies ranging from Intel to Nvidia. May 20, 21, and 23 will feature all-day seminars on topics including multimedia technologies and selecting processors for next-generation PCs.

Register by March 31 for special early rates. For more information, check our Web site ([www.chipanalyst.com](http://www.chipanalyst.com)) or call 800.527.0288 or 707.824.4001 (outside U.S.).

May 20–23

*Hyatt Regency San Francisco Airport*