RESOURCES

The ABCs of DSPs

DSP stalwart Berkeley Design Technology (BDT) has released a guide to DSP basics geared toward engineers and engineering managers. DSP Processor Fundamentals: Architectures and Features fills its 220 pages with introductory material on numerical formats, data paths, instruction sets, execution control, and pipelining.

Initial copies of the book list for \$60, with discounts available for volume purchases. For more information, or to order, call BDT (Fremont, Calif.) at 408.997.1842; fax 408.268.5623; or send e-mail to info@bdti.com.

Benchmarks for Batteries from BAPCo

The Business Applications Performance Corporation (BAPCo) has developed the SYSmark for Battery Life benchmark suite to measure and compare battery life in PC-compatible notebook computers. The scripted benchmarks run commercial business productivity applications the way a user might actually run them, including idle periods between applications.

The benchmark package, which costs \$995, is delivered on CD-ROM and includes a photocell for monitoring the intensity of the computer's screen and a mechanical actuator for pressing keys on the keyboard. For more information, contact BAPCo (Santa Clara, Calif.) at 408.988.7654; fax 408.765.4920; or visit www.bapco.com.

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Best PC Prices in Town!

The PC Street Price Index is a monthly newsletter published by Metro Computing that, as its name might imply, collates and lists the lowest street prices for several hundred different PC and Macintosh systems, hardware upgrades, peripherals, and software. Prices are collected from more than 1,500 retail, mailorder, catalog, and warehouse stores across the United States, and they are updated monthly.

A single issue of the newsletter sells for \$30; quarterly and yearly subscriptions are also available. For more information, or to subscribe, contact Metro Computing (Gibbsboro, N.J.) at 609.784.8866; fax 609.784.9814.

\$100 Off a Stanford Education

Stanford University's Western Institute of Computer Science is holding its summer 1996 program in computer science from July through August. Week-long classes include such topics as Internet security, advanced Java, object data management, electronic commerce, code optimizations, electronic commerce, C++, and video.

Registration 14 days in advance of the class costs \$1,450; regular tuition is \$1,575. A \$100 discount is available for multiple enrollments or for referring another attendee. For more information, contact WICS (Magalia, Calif.) at 916.873.0575; fax 916.873.6697; or visit www-wics.stanford.edu/WICS.html.

> MICRODESIGN Resources REVISED AND UPDATED!

New DRAM Technologies

A Comprehensive Analysis of the New Architectures

Today's processors demand more bandwidth and faster access to main memory than ever before. To meet this challenge, DRAM vendors are offering a new generation of memory solutions ranging from the conventional to the revolutionary. These dramatic developments make selecting the right DRAM for an application more complex than ever. This report delivers, in a single comprehensive volume, the information needed to understand the issues, see where the DRAM industry is going, and make the right choices. New DRAM Technologies was written by Steven Przybylski, Ph.D.

For more information, excerpts from the report, and a complete table of contents, visit our Web site (www.chipanalyst.com/tech_lib/dram.html) or call 800.527.0288 or 707.824.4001 (outside U.S.).