RESOURCES

Intel Developers Form in San Francisco

Gordon Moore, Pat Gelsinger, and Dan Russell will be among the presenters at the **Intel Developers' Forum**, to be held September 29 through October 1 at the Hyatt Regency Hotel near the San Francisco airport. The three-day conference is geared toward PC designers and features technical presentations, demonstrations, and dialogue with Intel officers and engineers.

Registration costs \$1,195 for all three days. For more information or to register, call Intel (Santa Clara, Calif.) at 800.886.5846 or visit *www.intel.com/intel/idf.*

Intel Goes Public With Technical Journal

Intel's previously internal publication, the *Intel Technical Journal*, is now available for public consumption. After 10 years as a quarterly internal company journal, current issues are now being published on the Web. Back issues and articles will be added as time allows. The first Web-based issue focuses on the research underlying the development of MMX.

To locate the *Intel Technical Journal*, point your browser to *developer.intel.com/technology/itj.*

How Do You Package a Roadmap?

Or create a roadmap for packages? Integrated Circuit Engineering knows and tells all in *Roadmaps of Packaging Technology*, a 580-page special report that covers the driving forces in IC

MICRODESIGN Resources

Microprocessor Forum

October 13-16, Fairmont Hotel, San Jose

More than 18 critical new technology announcements make the 1997 Microprocessor Forum a must. If you have a stake in the microprocessors and companies that are defining the future of the industry, you'll want to hear:

- Intel and Hewlett-Packard give the first technical disclosure of the IA-64 64-bit instruction set.
- Rambus disclose the specifications for Direct Rambus DRAM, the standard for PCs in 1999.
- · Intel disclose key features of Deschutes.
- Sun provide the first look at MicroJava, the first processor to directly execute Java bytecodes.
- AMD, Cyrix, and Centaur disclose the new processors that will keep them competitive in 1998.

Please register early. Due to the importance of the announcements at this year's event, we expect to be sold out well before the Forum. You may register on line at *www.MDRonline.com/mpf* or by phone at 707.824.4001 or 800.527.0288. Special registration discounts are available to subscribers of *Microprocessor Report*.

packaging, thermal requirements and constraints, chip bonding, grid-array packaging, interconnect substrate technologies, and more.

Copies of the book sell for \$850 in either printed or CD-ROM version; additional copies (in either format) are \$450 apiece. To order or for more information, contact ICE (Scotts-dale, Ariz.) at 602.515.9780 or visit *www.ice-corp.com/ice*.

■ How Do They Make Those Little ICs, Anyway? These and other questions for the less technically astute among us are answered in Integrated Circuit Engineering's easy-to-read publication, *Basic Integrated Circuit Technology Reference Manual*. At 208 large, friendly pages, this book introduces concepts such as memory and logic, digital and analog, chips and DIPs. It also contains more than 200 illustrations and 375 glossary words and is guaranteed to have no equations.

The book is available for \$335 (printed form only); additional copies are \$165 apiece, with large discounts given for orders of 50 copies or more (perfect for board meetings). To order, contact ICE (Scottsdale, Ariz.) at 602.515.9780 or visit *www.ice-corp.com/ice.*

Journal for Ada Aficionados

The *AdaIC News* from the Ada Information Clearinghouse is free. Call 800.232.4211 or visit *sw-eng.falls-church.va.us*.

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