RECENT IC ANNOUNCEMENTS

| PART NUM BER | VENDOR | DESCRIPTIO N | PRICE/Q UANTITY | AVAILABILITY |
| :---: | :---: | :---: | :---: | :---: |
| INTERFACE |  |  |  |  |
| NET2980 | $\begin{aligned} & \text { NetChip } \\ & 650.526 .1490 \end{aligned}$ | USB controller allows control over endpoint addresses, directions, and transfer types; with 64-byte FIFO per endpoint; in PQ FP-48. | \$3.50/150,000 | Prod.-Now |
| SYM $53 C 896$ | $\begin{aligned} & \text { Symbios } \\ & 800.856 .3093 \end{aligned}$ | PCI-to-UItra SCSI II interface handles 160M/s data rates, 64 -bit dualchannel UItra SCSI II, two independent channels at $80 \mathrm{M} / \mathrm{s}$. | \$110/10,000 | $\begin{aligned} & \text { Samples-Now } \\ & \text { Prod.-2Q } 98 \end{aligned}$ |
| OTI-9150 | $\begin{aligned} & \text { Oak } \\ & 408.737 .0888 \end{aligned}$ | CD-ROM block decoder uses Ultra DM interface and supports drive speeds up to 40x; performs error correction, to 33M /s transfer rate. | \$4.50/10,000 | Prod.-Now |
| MEMORY |  |  |  |  |
| $24 \times x 128$ | Microchip $602.786 .7668$ | Family of $E^{2}$ PROM devices with $I^{2} C$ interfaces are housed in small 8-lead packages. | \$3.12/1,000 | Prod.-Now |
| $24 C 00$ | Microchip $602.786 .7668$ | Serial E2PROM has ${ }^{2}$ 2 interface, 16 -byte data capacity, and small, 5 -lead SOT-23 package that measures $0.11 \times 0.059$ inch. | \$1/1,000 | Prod.-Now |
| M 29Fx00 | $\begin{aligned} & \text { SGS-Thomson } \\ & 781.259 .0300 \end{aligned}$ | Flash memory devices come in $1-2-$, and $4-\mathrm{M}$ bit capacities with boot block, 5 -V operation, and configurable 8- or 16-bit bus. | \$3.00/10,000 | Prod.-Now |
| M 29W xxx | $\begin{aligned} & \text { SGS-Thomson } \\ & 781.259 .0300 \end{aligned}$ | Flash memory devices have 4-M bit capacity, low power consumption, and fast erase/ program time; with or without book-block feature. | \$6.00/10,000 | Prod.-Now |
| $\begin{aligned} & \text { 24LCS61 } \\ & \text { 24LCS62 } \end{aligned}$ | Microchip 602.786.7668 | Serial $1^{2}$ C E2PROM s have 1 K and 2 K capacity and software addressing so up to 255 devices can be used on a board. | \$0.95/1,000 | Prod.-Now |
| M ISCELLANEO US |  |  |  |  |
| DS2437 | $\begin{aligned} & \text { Dallas Semi } \\ & 972.371 .4448 \end{aligned}$ | Battery-management device includes all Smart Battery features, including temperature, voltage, current, and time monitoring; ROM , A/D, more. | \$3.95/10,000 | Prod.-Now |
| ZPSD211RV | $\begin{aligned} & \text { WSI } \\ & 510.656 .5400 \end{aligned}$ | Programmable microcontroller peripheral has 32 K EPROM , decoder, PLD, $1 / 0$ pins; part draws only 1.4 mA at 2 M Hz . | \$2.19/100,000 | Prod.-Now |
| TSIxxxB1 | $\begin{aligned} & \text { SGS-Thomson } \\ & 781.259 .0300 \end{aligned}$ | Telephone-set interface chip integrates diode bridge, line-interface function, and surge protection; crowbar standoff ranges from 62 V to 265 V . | \$0.38/500,000 | Prod.-Now |
| $\begin{aligned} & \text { L4955 } \\ & \text { L4956 } \end{aligned}$ | $\begin{aligned} & \text { SGS-Thomson } \\ & 781.259 .0300 \end{aligned}$ | Low-dropout voltage regulators for Pentium II and Pentium Pro systems have 150 -milliohm on resistance for 5 A of current with $750-\mathrm{mV}$ dropout. | \$1.50/5,000 | Prod.-Now |
| OTI-257 | $\begin{aligned} & \text { Oak } \\ & 408.737 .0888 \end{aligned}$ | Single-chip VideoCD karaoke controller combines M PEG-1 audio/video decoder, audio DSP, on-screen display, and TV encoder. | \$15/10,000 | Prod.-Now |
| PRO GRAM M ABLE LO GIC |  |  |  |  |
| EPM 9560A | Altera $408.544 .7000$ | Product-term-based PLD has 10-ns pin-to-pin delay; in 0.5 -micron CM OS process; 560 macrocells compatible with M AX9000 family of parts. | \$29.75/10,000 | Samples-Now <br> Prod.-4Q98 |
| XCSxx | Xilinx $408.559 .7778$ | Family of Spartan FPGAs for high-volume applications has gate density to 40,000 gates; from 238 to 1,862 logic cells, includes on-chip RAM. | \$3.95/100,000 | Samples-Now <br> Prod.-4Q09 |
| A42M X09 | $\begin{aligned} & \text { Actel } \\ & 408.739 .1010 \end{aligned}$ | FPGA has 5.6-ns clock-to-output delay, mixed 3.3-V and 5-V operation; in $84-160$-, and 176 -lead packages. | \$5.90/10,000 | Prod.-Now |

