RECENT IC ANNOUNCEMENTS

| PART NUM BER | VENDOR | DESCRIPTIO N | PRICE/Q UANTITY | AVAILABILITY |
| :---: | :---: | :---: | :---: | :---: |
| M ICRO PRO CESSO R |  |  |  |  |
| 68HC705KJ1 | $\begin{aligned} & \text { M otorola } \\ & 800.765 .7795 \end{aligned}$ | M icrocontroller has $68 \mathrm{HCO5}$ CPU core, 1,240 bytes of OTPRO M, 64 bytes of RAM , 10 bidirectional I/O pins; in 16 -pin SOIC or DIP. | \$0.50/500,000 | Prod.-Now |
| $\begin{aligned} & \text { SX18AC } \\ & \text { SX28AC } \end{aligned}$ | $\begin{aligned} & \text { Scenix } \\ & \text { 408.327.8888 } \end{aligned}$ | Pair of 8 -bit microcontrollers run at 50 MHz , with 2 K of flash memory, oscillator, power management; in 18 - and 28 -lead packages. | \$3.49/1,000 | Prod.-Now |
| 8051XA-S3 | $\begin{aligned} & \text { Philips } \\ & 800.447 .1500 \end{aligned}$ | Newest 8051-compatible processor has 10-bit A/D converter, programmable counter array, ${ }^{12} \mathrm{C}$ interface, and 32 K of EPROM. | \$8/10,000 | $\begin{aligned} & \text { Samples-Now } \\ & \text { Prod.-2Q } 98 \end{aligned}$ |
| INTERFACE |  |  |  |  |
| PI2C6040 | $\begin{aligned} & \text { Pericom } \\ & 408.435 .0800 \end{aligned}$ | Four-port integrated Ethernet physical-layer (PHY) interface; with four ports, part is suitable for hubs and switches. | \$9.95/10,000 | Prod.-Now |
| $\begin{aligned} & \text { S3041 } \\ & \text { S3042 } \end{aligned}$ | $\begin{aligned} & \text { AMCC } \\ & 800.755 .2622 \end{aligned}$ | Eight-bit transmitter ('41) and receiver ('42) for SO NET/SDH/ATM applications in $2.4-\mathrm{GHz}$ O C-48 systems; in 100-lead TQFP. | \$110/100 | $\begin{aligned} & \text { Samples-Now } \\ & \text { Prod.-3Q } 98 \end{aligned}$ |
| M EM ORY |  |  |  |  |
| $\begin{aligned} & \text { M 48Z2M 1Y } \\ & \text { M 48Z512A/Y } \end{aligned}$ | $\begin{aligned} & \text { SGS-Thomson } \\ & 781.259 .0300 \end{aligned}$ | Zero-power SRAM s have 16-M bit ('2M 1) and 4-M bit ('512) capacity, built-in battery, 600 -mil DIP package, 10 -year data retention. | \$44/1,000 | Prod.-Now |
| $\begin{aligned} & \text { 24LC256 } \\ & \text { 24AA256 } \end{aligned}$ | Microchip 602.786.7668 | Serial E2PRO M s have fast, 5 -ms write-cycle time and 256-Kbit capacity, and run from $2.5-\mathrm{V}$ ('LC) or $1.8-\mathrm{V}$ ('AA) power supplies. | \$2.98/1,000 | Prod.-Now |
| M CM 63773x | $\begin{aligned} & \text { M otorola } \\ & 512.933 .8705 \end{aligned}$ | Fast static RAM s have 4-M bit capacity and zero bus-turnaround capability to eliminate dead cycles between writes and reads; in $\times 36$ organization. | \$39/1,000 | $\begin{aligned} & \text { Samples-Now } \\ & \text { Prod.-2Q } 98 \end{aligned}$ |
| M CM 63781x | $\begin{aligned} & \text { M otorola } \\ & \text { 512.933.8705 } \end{aligned}$ | Fast static RAM s have 4-M bit capacity and zero bus-turnaround capability to eliminate dead cycles between writes and reads; in $\times 18$ organization. | \$39/1,000 | $\begin{aligned} & \text { Samples-Now } \\ & \text { Prod.-2Q } 98 \end{aligned}$ |
| M ISCELLANEO US |  |  |  |  |
| EVCR PRO | Lucent 800.372.2447 | Variable-rate codec for CDM A telephones integrates DSP, voice codec, and 72 Kwords of ROM ; runs at 100 M Hz from 3.3 V . | \$50/1 | $\begin{aligned} & \text { Samples-Now } \\ & \text { Prod.-3Q } 98 \end{aligned}$ |
| M SM 9831 | $\begin{aligned} & \text { Oki } \\ & \text { 408.737.6445 } \end{aligned}$ | Speech-synthesis chip can generate voice prompts or mechanical sound effects; $4-\mathrm{kHz}$ to $16-\mathrm{kHz}$ sampling frequencies; 11 seconds playback time. | \$1.15/10,000 | $\begin{aligned} & \text { Samples-Now } \\ & \text { Prod.-3Q } 98 \end{aligned}$ |
| VCX162xx | $\begin{aligned} & \text { Pericom } \\ & 408.435 .0800 \end{aligned}$ | Family of 16 -bit buffers and transceivers for $2.5-\mathrm{V}$ systems such as PCs, with $24-\mathrm{mA}$ output drive and $2.5-\mathrm{ns}$ propagation delays. | \$2.05/1,000 | Prod.-Now |
| P16C671 | $\begin{aligned} & \text { Pericom } \\ & 408.435 .0800 \end{aligned}$ | Clock synthesizer for Pentium and Pentium II systems has two PLLS, 2.5-3.3-V operation, seven synchronous PCI clocks, eight SDRAM clocks. | \$2.50/10,000 | Prod.-Now |
| PRO GRAM M ABLE LO GIC |  |  |  |  |
| EPF6024A | Altera $408.544 .7000$ | Programmable device has 24,000 gates of logic, 3.3-V operation, 1,960 logic elements; built in 0.35 -micron SRAM process. | \$9.50/10,000 | $\begin{aligned} & \text { Samples-Now } \\ & \text { Prod.-4Q98 } \end{aligned}$ |
| Flash370i | $\begin{aligned} & \text { Cypress } \\ & 408.943 .2600 \end{aligned}$ | Family of programmable devices has 3.3 -V-tolerant I/ 0 pins, in-system programmability using flash memory cells. | \$1.25/10,000 | Prod.-Now |

