

DS1285, DS1285Q Real Time Clock

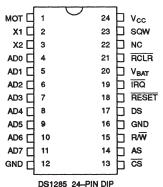
FEATURES

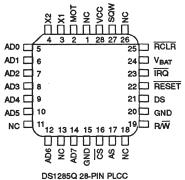
- Drop-in replacement for IBM AT computer clock/calendar
- Pin configuration closely matches MC146818A
- · Counts seconds, minutes, hours, days, day of the week, date, month, and year with leap year compensation
- Binary or BCD representation of time, calendar, and alarm
- 12- or 24-hour clock with AM and PM in 12-hour mode
- Daylight Savings Time option
- Selectable between Motorola and Intel bus timing
- Multiplex bus for pin efficiency
- Interfaced with software as 64 RAM locations
 - 14 bytes of clock and control registers
 - 50 bytes of general purpose RAM
- Programmable square wave output signal
- Bus compatible interrupt signals (IRQ)
- Three interrupts are separately software-maskable and testable
- Time-of-day alarm once/second to once/day
- Periodic rates from 122 µA to 500 ms
- End of clock update cycle
- Optional 28-pin PLCC surface mount package

DESCRIPTION

The DS1285 Real Time Chip is a direct replacement for the MC146818A in IBM AT computer clock/calendar and other applications. An external crystal and battery are the only components required to maintain time-of-day and memory status in the absence of power. For a complete description of operating conditions, electrical characteristics, bus timing, and pin descriptions other than X1, X2, VBAT, and RCLR, see the DS1287 data sheet.

PIN ASSIGNMENT





PIN DESCRIPTION

AD0-AD7	-	Multiplexed Address/Data Bus

NC No Connection **Bus Type Selection** MOT CS Chip Select AS Address Strobe R/W Read/Write Input DS **Data Strobe** RESET Reset Input

IRQ Interrupt Request Output SQW Square Wave Output +5 Volt Supply

 V_{CC} GND Ground

32.768 kHz Crystal Connections X1,X2

+3 Volt Battery Input

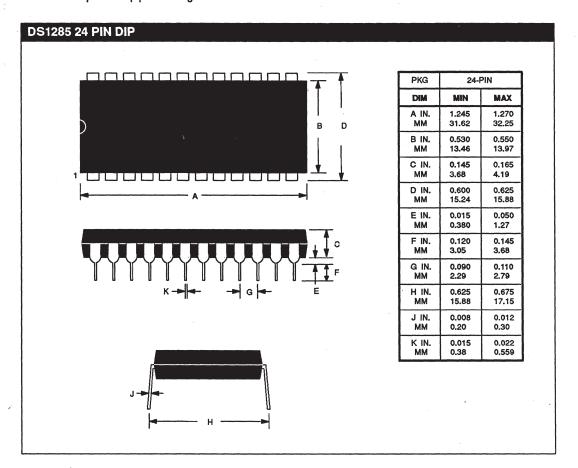
RAM Clear

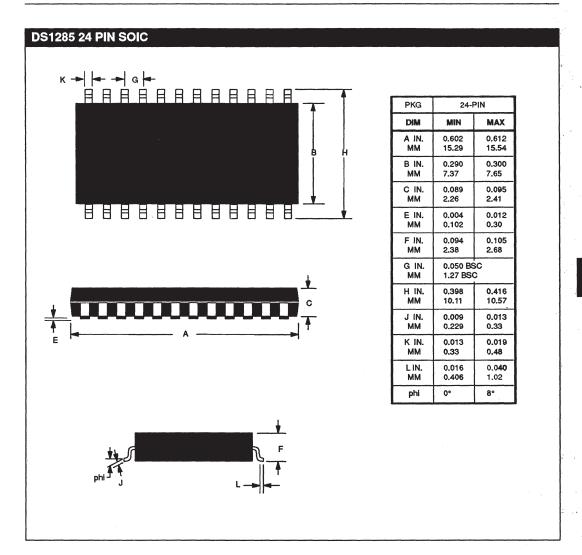
PIN DESCRIPTION

X1, X2 - Connections for a standard 32.768 kHz quartz crystal, Daiwa part number DT-26S, Seiko part number DS-VT-200, or equivalent. The internal oscillator circuitry is designed for operation with a crystal having a specified load capacitance (CL) of 6 pF. A variable trimming capacitor may be required for extremely high precision timekeeping applications. Crystals can be ordered from Dallas Semiconductor. Order part number DS9032.

V_{BAT} - Battery input for any standard 3 volt lithium cell or other energy source. Battery voltage must be held between 2.5 and 3.7 volts for proper operation. The nominal write protect trip point voltage at which access to the real time clock and user RAM is denied is set by the internal circuitry as 1.26 x V_{BAT} . A maximum load of .5 μ A at 25°C in the absence of power should be used to size the external energy source.

 $\overline{\text{RCLR}}$ - The $\overline{\text{RCLR}}$ pin is used to clear (set to logic 1) all 50 bytes of general purpose RAM but does not affect the RAM associated with the real time clock. In order to clear the RAM, $\overline{\text{RCLR}}$ must be forced to an input logic 0 (-0.3 to +0.8 volts) during battery back-up mode when V_{CC} is not applied. The $\overline{\text{RCLR}}$ function is designed to be used via human interface (shorting to ground manually or by switch) and not to be driven with external buffers. This pin is internally pulled up.





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DS1285Q 28 PIN PLCC .010 Fl 010. PKG 28-PIN DIM MIN MAX 0.300 REF 7.62 A IN. MM B IN. MM 0.442 17.68 B1 IN. MM 0.013 0.33 0.021 0.53 0.090 2.29 0.120 3.05 F IN. MM 0.020 0.51 MIN MIN 0.430 10.92