

8K x 8 Bit Static RAM

FEATURES

- Fast Access Time 35, 45, 55ns (max.)
- Low Power Dissipation  
Standby (TTL): 2mA (max.)  
(CMOS): 100µA (max.)  
Operating : 100mA (max.)
- Single 5V ± 10% supply
- TTL compatible inputs and outputs
- Full Static Operation  
—No clock or refresh required
- Common I/O, Tristate Output
- Low Data Retention Current: 50µA (max.)
- Battery Back-up Operation  
—2V (min.) Data Retention
- Standard 28-pin DIP (300 mil)

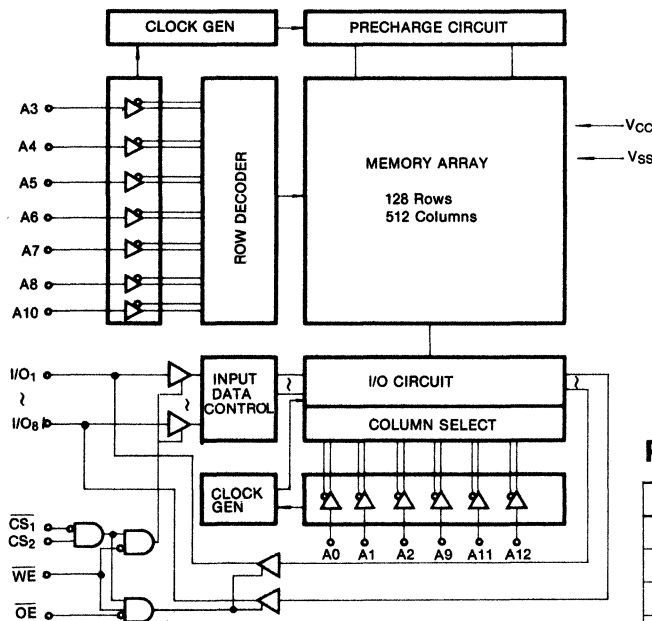
GENERAL DESCRIPTION

The KM6865 is a 65,536-bit high speed Static Random Access Memory organized as 8,192 words by 8 bits. The device is fabricated using Samsung's advanced CMOS process.

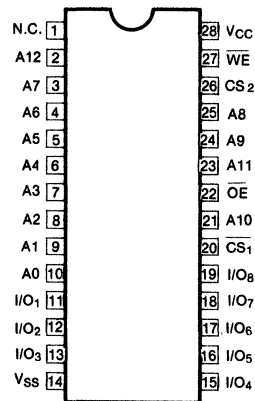
The KM6865 has an output enable input for precise control of the data outputs. It also has chip enable inputs for the minimum current power down mode.

The KM6865 has been designed for high speed applications. It is particularly well suited for the use in high speed and low power applications in which battery back up for nonvolatility is required.

FUNCTIONAL BLOCK DIAGRAM



PIN CONFIGURATION



PIN NAMES

| Pin Name                           | Pin Function        |
|------------------------------------|---------------------|
| A <sub>0</sub> -A <sub>12</sub>    | Address Inputs      |
| WE                                 | Write Enable        |
| CS <sub>1</sub> , CS <sub>2</sub>  | Chip Select         |
| OE                                 | Output Enable       |
| I/O <sub>1</sub> -I/O <sub>8</sub> | Data Inputs/Outputs |
| V <sub>CC</sub>                    | +5V Power Supply    |
| V <sub>SS</sub>                    | Ground              |
| N.C.                               | No Connection       |